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c6i.4xlarge

amazon testing on Amazon Linux 2 via the Phoronix Test Suite.

Test Systems:

c6i.4xlarge

Processor: Intel Xeon Platinum 8375C (8 Cores / 16 Threads), Motherboard: Amazon EC2 c6i.4xlarge (1.0 BIOS), Chipset: Intel 440FX 82441FX PMC, Memory: 1 x 32 GB DDR4-3200MT/s, Disk: 21GB Amazon Elastic Block Store, Network: Amazon Elastic

OS: Amazon Linux 2, Kernel: 5.10.167-147.601.amzn2.x86_64 (x86_64), Compiler: GCC 10.4.1 20221124, File-System: xfs, System Layer: amazon

Kernel Notes: Transparent Huge Pages: madvise
Compiler Notes: --build=x86_64-redhat-linux --disable-libunwind-exceptions --enable-__cxa_atexit --enable-bootstrap --enable-checking=release --enable-gnu-indirect-function --enable-gnu-unique-object --enable-initfini-array --enable-languages=c,c++,fortran,lto --enable-multilib --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --program-prefix=gcc10- --with-arch_32=x86-64 --with-as=/usr/bin/gcc10-as --with-gcc-major-version-only --with-isl --with-ld=/usr/bin/gcc10-ld --with-linker-hash-style=gnu
Processor Notes: CPU Microcode: 0xd000375
Security Notes: itlb_multihit: Not affected + l1tf: Not affected + mds: Not affected + meltdown: Not affected + mmio_stale_data: Mitigation of Clear buffers; SMT Host state

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unknown + retbleed: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Enhanced IBRS IBPB: conditional RSB filling + srbds: Not affected + tsx_async_abort: Not affected

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BYTE Unix Benchmark - Dhrystone 2 (LPS)	40913499
Standard Deviation	1.1%
RAMspeed SMP - Add - Integer (MB/s)	28224
Standard Deviation	0.1%
RAMspeed SMP - Copy - Integer (MB/s)	26965
Standard Deviation	1.2%
RAMspeed SMP - Scale - Integer (MB/s)	23071
Standard Deviation	0.2%
RAMspeed SMP - Triad - Integer (MB/s)	28397
Standard Deviation	0.8%
RAMspeed SMP - Average - Integer (MB/s)	26631
Standard Deviation	1.3%
RAMspeed SMP - Add - Floating Point (MB/s)	25837
Standard Deviation	0.5%
RAMspeed SMP - Copy - Floating Point (MB/s)	26550
Standard Deviation	0.4%
RAMspeed SMP - Scale - Floating Point (MB/s)	23303
Standard Deviation	0.3%
RAMspeed SMP - Triad - Floating Point (MB/s)	27619
Standard Deviation	0.3%
RAMspeed SMP - Average - Floating Point (MB/s)	25844
Standard Deviation	0.6%
CacheBench - Read (MB/s)	2958
Standard Deviation	0%
CacheBench - Write (MB/s)	24289
Standard Deviation	0%
CacheBench - R.M.W (MB/s)	38406
Standard Deviation	0%
Hackbench - 1 - Thread (sec)	3.392
Standard Deviation	0.9%
Hackbench - 2 - Thread (sec)	6.089
Standard Deviation	6.8%
Hackbench - 4 - Thread (sec)	9.851
Standard Deviation	1.1%
Hackbench - 8 - Thread (sec)	17.895
Standard Deviation	0.4%
Hackbench - 1 - Process (sec)	3.130
Standard Deviation	0.3%
Hackbench - 16 - Thread (sec)	31.203
Standard Deviation	0.7%
Hackbench - 2 - Process (sec)	5.243
Standard Deviation	0.3%
Hackbench - 32 - Thread (sec)	63.277
Standard Deviation	1.2%
Hackbench - 4 - Process (sec)	9.075
Standard Deviation	0.2%

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Hackbench - 8 - Process (sec) 16.890

Standard Deviation 0.2%

Hackbench - 16 - Process (sec) 30.089

Standard Deviation 2.3%

Hackbench - 32 - Process (sec) 59.874

Standard Deviation 1.2%

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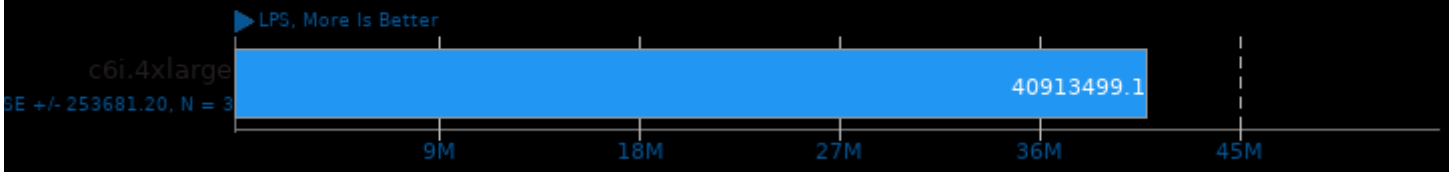
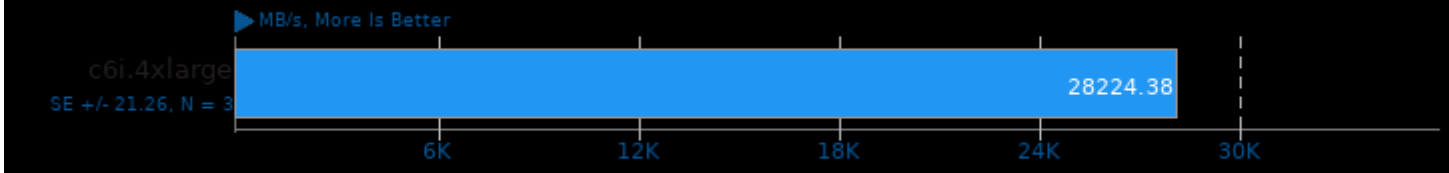
BYTE Unix Benchmark 3.6

Computational Test: Dhrystone 2



RAMspeed SMP 3.5.0

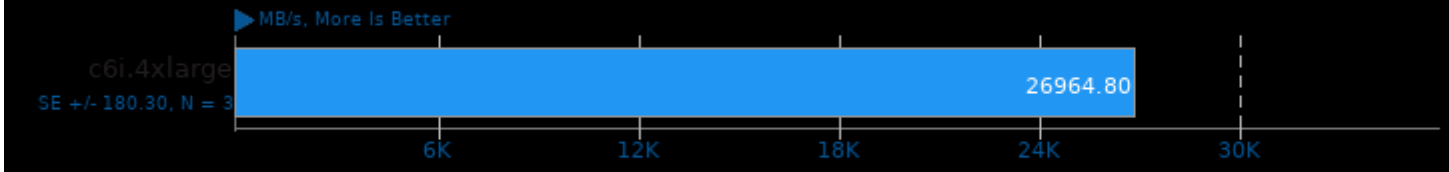
Type: Add - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

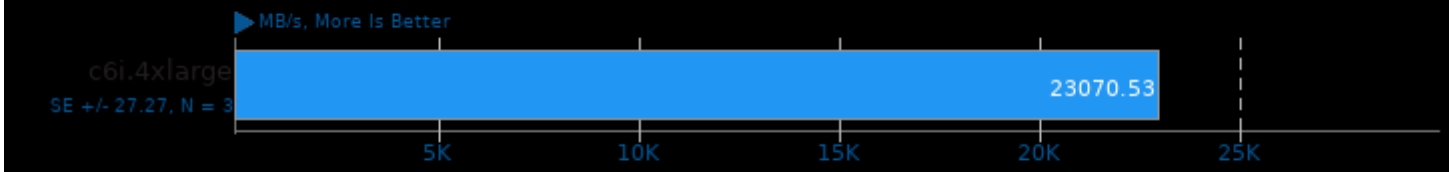
Type: Copy - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

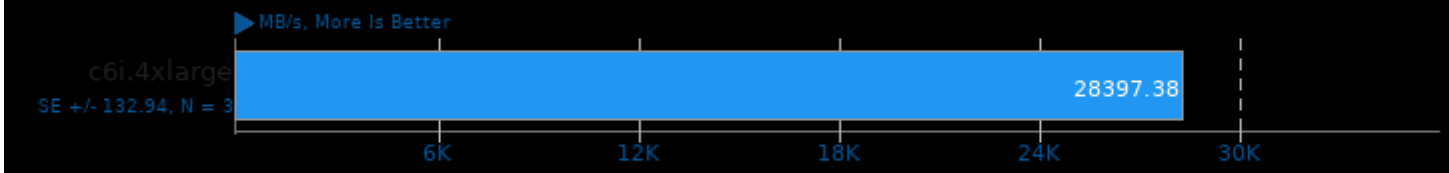
Type: Scale - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

Type: Triad - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

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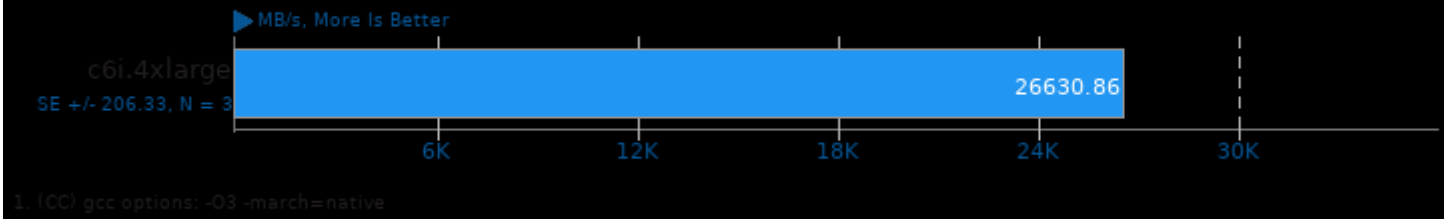
RAMspeed SMP 3.5.0

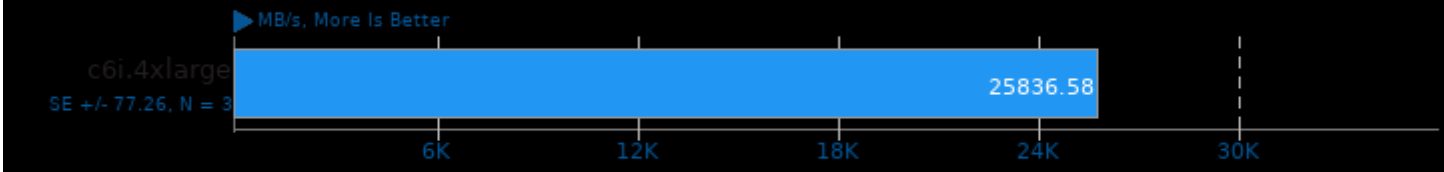
Type: Average - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

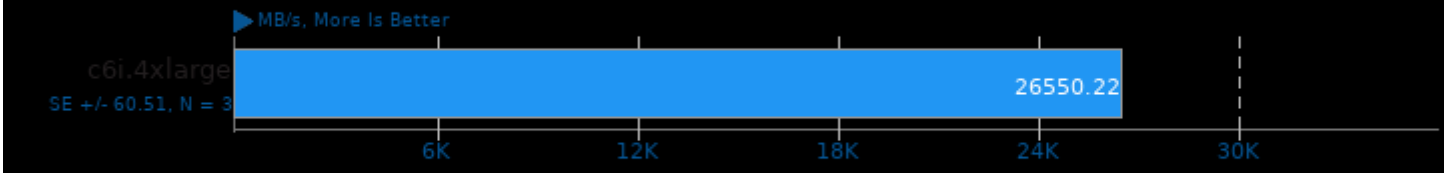
Type: Add - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

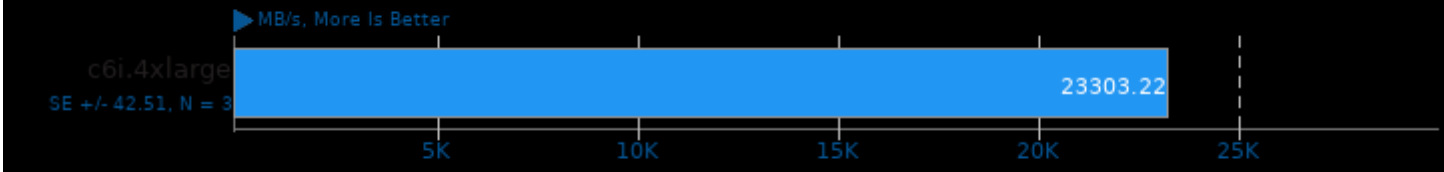
Type: Copy - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

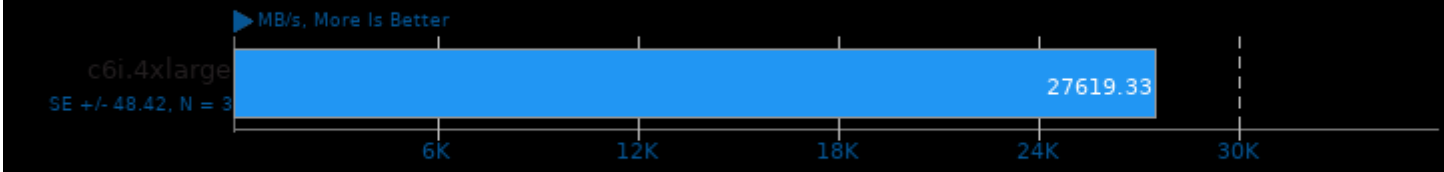
Type: Scale - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

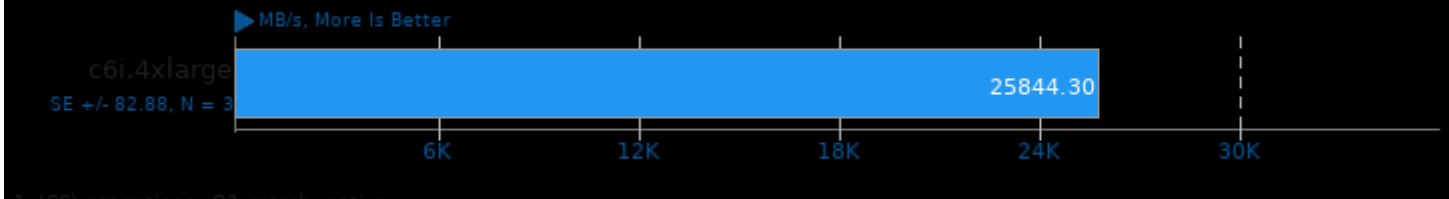
Type: Triad - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

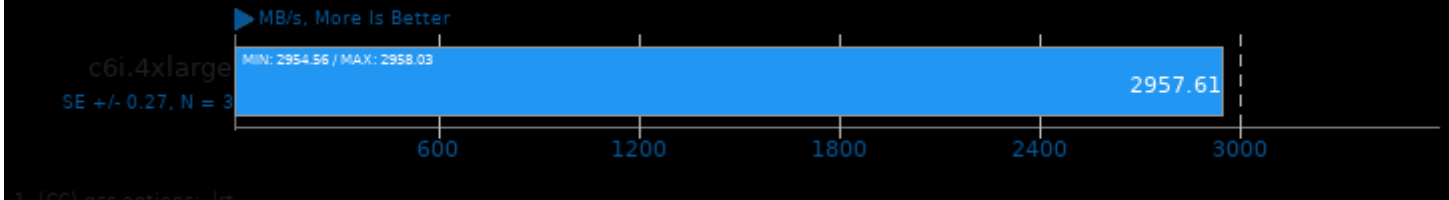
Type: Average - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

CacheBench

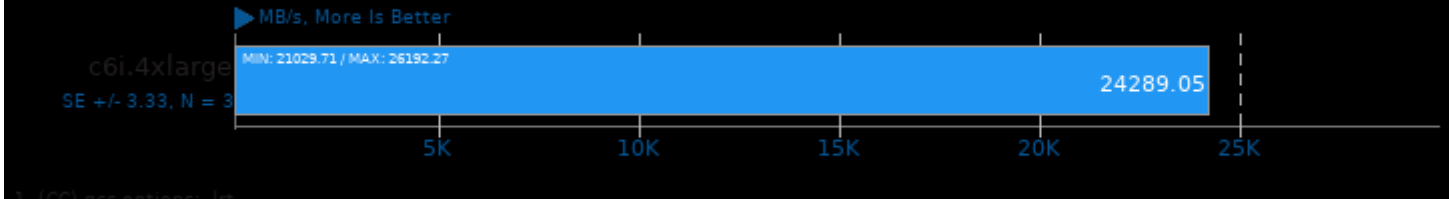
Test: Read



1. (CC) gcc options: -lrt

CacheBench

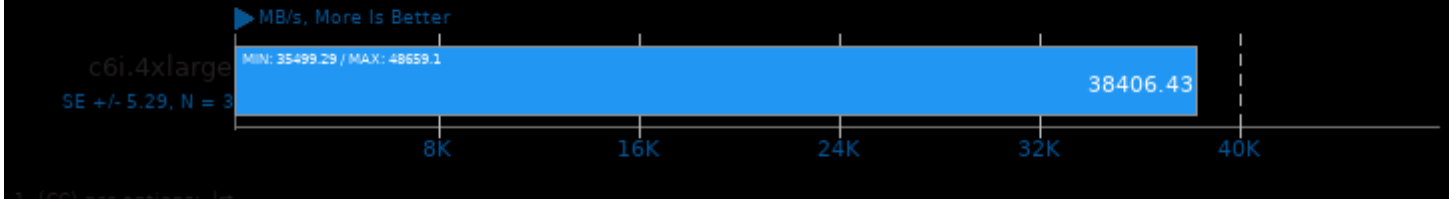
Test: Write



1. (CC) gcc options: -lrt

CacheBench

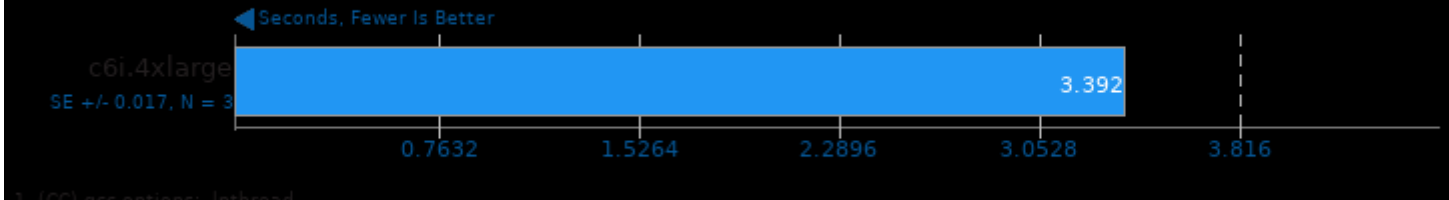
Test: Read / Modify / Write



1. (CC) gcc options: -lrt

Hackbench

Count: 1 - Type: Thread



1. (CC) gcc options: -lpthread

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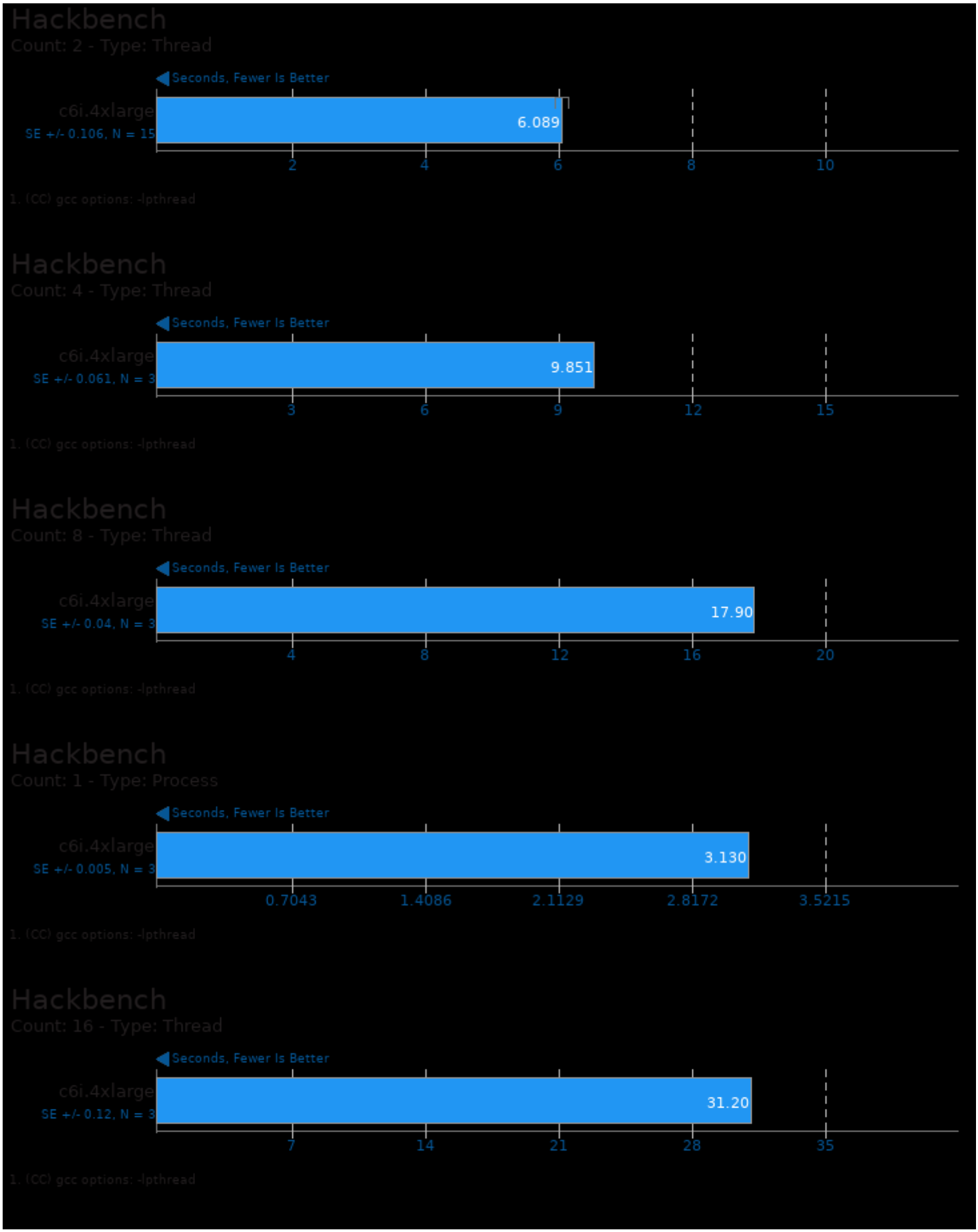
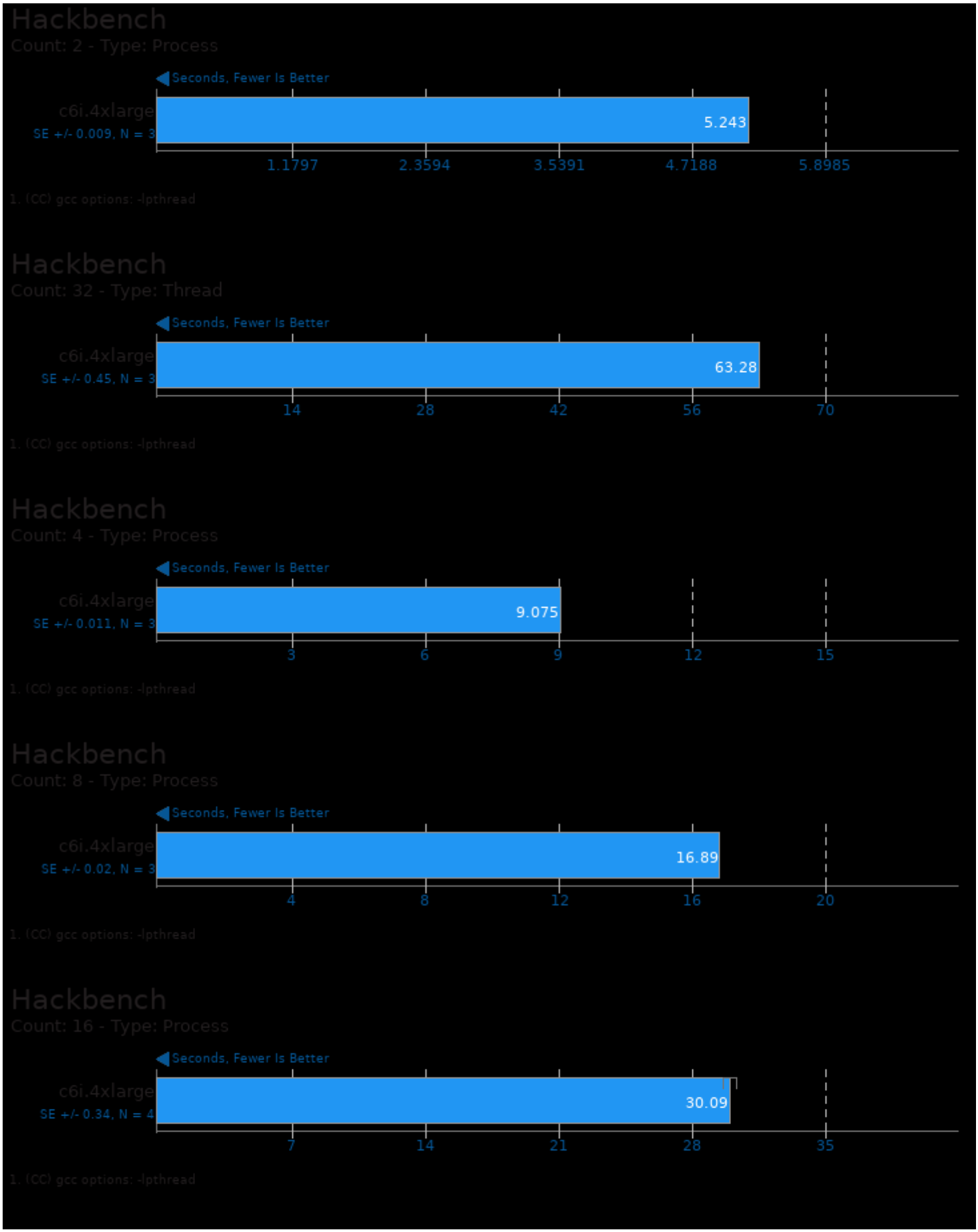
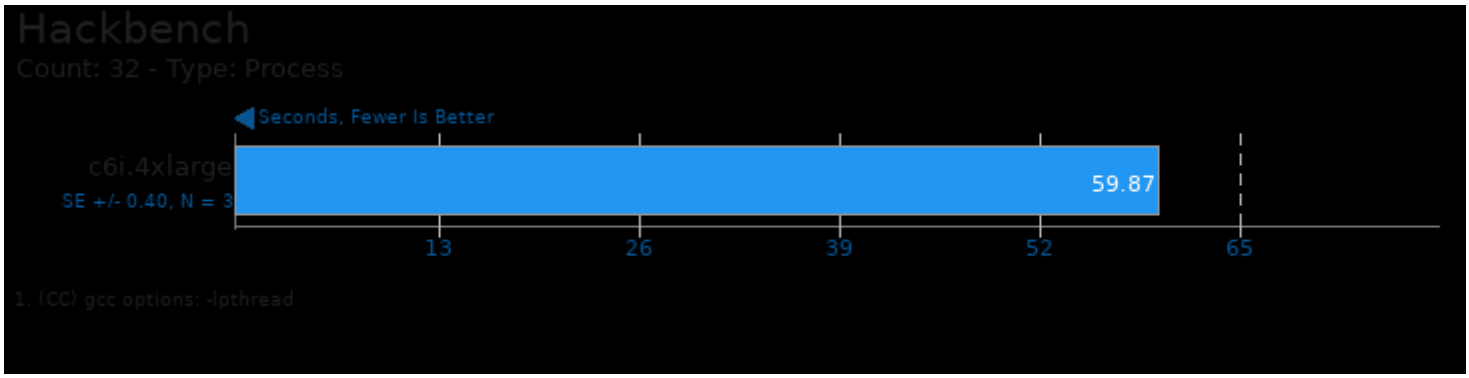

Hackbench
Count: 2 - Type: Thread
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.106, N = 15
6.089
2
4
6
8
10
1. (CC) gcc options: -lpthread
Hackbench
Count: 4 - Type: Thread
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.061, N = 3
9.851
3
6
9
12
15
1. (CC) gcc options: -lpthread
Hackbench
Count: 8 - Type: Thread
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.04, N = 3
17.90
4
8
12
16
20
1. (CC) gcc options: -lpthread
Hackbench
Count: 1 - Type: Process
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.005, N = 3
3.130
0.7043
1.4086
2.1129
2.8172
3.5215
1. (CC) gcc options: -lpthread
Hackbench
Count: 16 - Type: Thread
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.12, N = 3
31.20
7
14
21
28
35
1. (CC) gcc options: -lpthread

c6i.4xlarge


Hackbench
Count: 2 - Type: Process
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.009, N = 3
5.243
1.1797
2.3594
3.5391
4.7188
5.8985
1. (CC) gcc options: -lpthread
Hackbench
Count: 32 - Type: Thread
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.45, N = 3
63.28
14
28
42
56
70
1. (CC) gcc options: -lpthread
Hackbench
Count: 4 - Type: Process
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.011, N = 3
9.075
3
6
9
12
15
1. (CC) gcc options: -lpthread
Hackbench
Count: 8 - Type: Process
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.02, N = 3
16.89
4
8
12
16
20
1. (CC) gcc options: -lpthread
Hackbench
Count: 16 - Type: Process
Seconds, Fewer Is Better
c6i.4xlarge
SE +/- 0.34, N = 4
30.09
7
14
21
28
35
1. (CC) gcc options: -lpthread

c6i.4xlarge



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