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cpu_all

VMware testing on OracleServer 8.6 via the Phoronix Test Suite.

Test Systems:

2 x Intel Xeon E5-2695 v3

Processor: 2 x Intel Xeon E5-2695 v3 (3 Cores), Motherboard: Intel 440BX (6.00 BIOS), Chipset: Intel 440BX/ZX/DX, Memory: 1 x 4 GB DRAM, Disk: 107GB Virtual disk, Graphics: VMware SVGA II, Network: 3 x VMware VMXNET3

OS: OracleServer 8.6, Kernel: 5.15.0-1.43.4.1.el8uek.x86_64 (x86_64), Compiler: GCC 8.5.0 20210514, File-System: ext4, Screen Resolution: 800x600, System Layer: VMware

Kernel Notes: Transparent Huge Pages: always
Compiler Notes: --build=x86_64-redhat-linux --disable-libmpx --disable-libunwind-exceptions --enable-__cxa_atexit --enable-bootstrap --enable-cet --enable-checking=release --enable-gnu-indirect-function --enable-gnu-unique-object --enable-initfini-array --enable-languages=c,c++,fortran,ito --enable-multilib --enable-offload-targets=nvptx-none --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --with-arch_32=x86-64 --with-gcc-major-version-only --with-isl --with-linker-hash-style=gnu --with-tune=generic --without-cuda-driver
Processor Notes: CPU Microcode: 0x2006b06

Security Notes: itlb_multihit: KVM: Mitigation of VMX unsupported + I1tf: Mitigation of PTE Inversion + mds: Mitigation of Clear buffers; SMT Host state unknown + meltdown: Mitigation of PTI + mmio_stale_data: Not affected + retbleed: Mitigation of IBRS + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp +

spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of IBRS IBPB: conditional RSB filling + srbds: Not affected + tsx_async_abort: Not affected

2 x Intel Xeon E5-2695 v3**NAMD - ATPase Simulation - 327,506 Atoms (days/ns)** 15.01193

Standard Deviation 0.7%

asmFish - 1.H.M.2.D (Nodes/s) 2841343

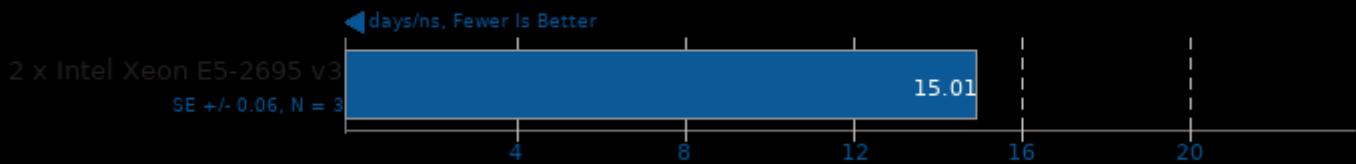
Standard Deviation 1%

Radiance Benchmark - Serial (sec) 1240**Radiance Benchmark - SMP Parallel (sec)** 742.922**ctx_clock - C.S.T (Clocks)** 1914

Standard Deviation 0.2%

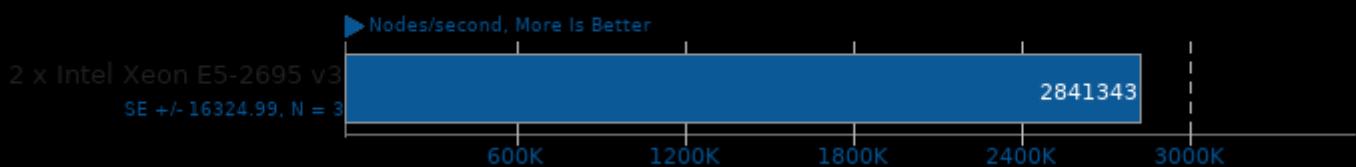
NAMD 2.14

ATPase Simulation - 327,506 Atoms



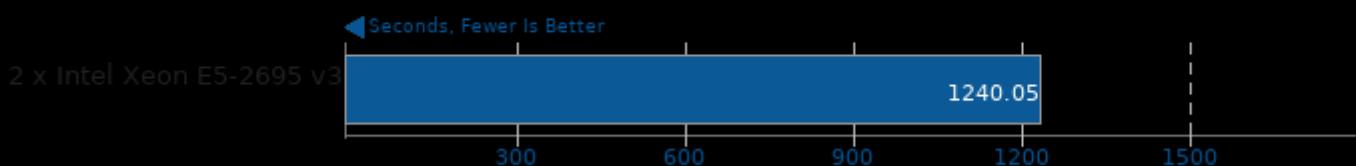
asmFish 2018-07-23

1024 Hash Memory, 26 Depth



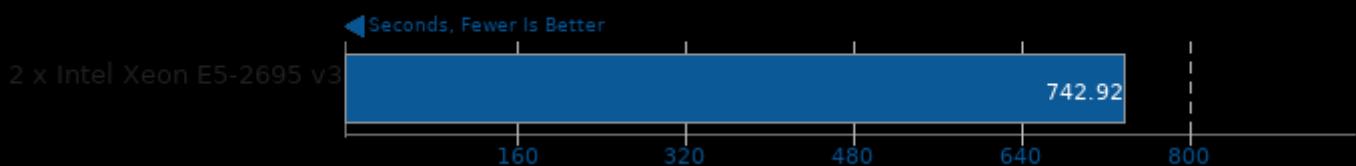
Radiance Benchmark 5.0

Test: Serial



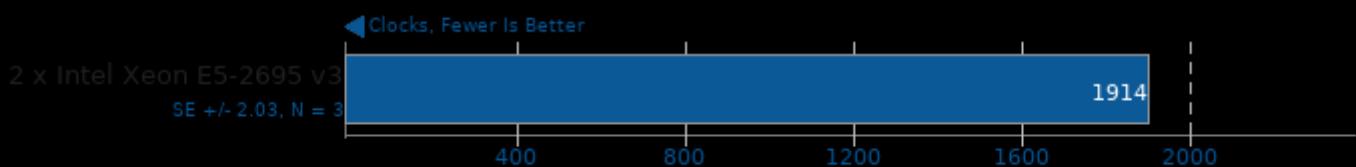
Radiance Benchmark 5.0

Test: SMP Parallel



ctx_clock

Context Switch Time



This file was automatically generated via the Phoronix Test Suite benchmarking software on Thursday, 28 March 2024 16:46.