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bmcd-ryzen5-sysbench

AMD Ryzen 5 3500U testing with a MOTILE PF4PU1F (N.1.06 BIOS) and AMD Picasso on Ubuntu 20.04 via the Phoronix Test Suite.

Test Systems:

AMD Ryzen 5 3500U - AMD Picasso - MOTILE PF4PU1F

Processor: AMD Ryzen 5 3500U @ 2.10GHz (4 Cores / 8 Threads), Motherboard: MOTILE PF4PU1F (N.1.06 BIOS), Chipset: AMD Raven/Raven2, Memory: 1 x 32 GB DDR4-2400MT/s Samsung M471A4G43MB1-CTD, Disk: 500GB CT500P1SSD8, Graphics: AMD Picasso (1200/1200MHz), Audio: AMD Raven/Raven2/Fenghuang, Monitor: DELL S3219D, Network: Realtek RTL8111/8168/8411 + Intel 8260

OS: Ubuntu 20.04, Kernel: 5.4.0-104-generic (x86_64), Desktop: GNOME Shell 3.36.9, Display Server: X Server 1.20.11, Vulkan: 1.2.182, Compiler: GCC 9.4.0, File-System: ext4, Screen Resolution: 2560x1440

Kernel Notes: Transparent Huge Pages: madvise
Compiler Notes: --build=x86_64-linux-gnu --disable-vtable-verify --disable-werror --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++,gm2 --enable-libstdcxx-debug --enable-libstdcxx-time=yes --enable-multiarch

--enable-multilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none=/build/gcc-9-Av3uEd/gcc-9-9.4.0/debian/tmp-nvptx/usr,hsa --enable-plugin --enable-shared --enable-threads=posix --host=x86_64-linux-gnu --program-prefix=x86_64-linux-gnu- --target=x86_64-linux-gnu --with-abi=m64 --with-arch-32=i686 --with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multilib-list=m32,m64,mx32 --with-target-system-zlib=auto --with-tune=generic --without-cuda-driver -v
Processor Notes: Scaling Governor: acpi-cpufreq performance (Boost: Enabled) - CPU Microcode: 0x8108102
Security Notes: itlb_multihit: Not affected + l1tf: Not affected + mds: Not affected + meltdown: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of LFENCE IBPB: conditional STIBP: disabled RSB filling + srbds: Not affected + tsx_async_abort: Not affected

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