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bmcd-unknown-sysbench

Intel Core i5-7500T testing with a Dell OptiPlex 7050 [0D24M8] (1.18.0 BIOS) and Intel HD 630 on Debian 11 via the Phoronix Test Suite.

Test Systems:

Intel Core i5-7500T - Intel HD 630 - Dell OptiPlex

Processor: Intel Core i5-7500T @ 3.30GHz (4 Cores), Motherboard: Dell OptiPlex 7050 [0D24M8] (1.18.0 BIOS), Chipset: Intel Xeon E3-1200 v6/7th + Q270, Memory: 2 x 16 GB DDR4-2400MT/s, Disk: Samsung SSD 980 PRO 500GB + 500GB 3.0, Graphics: Intel HD 630 (1100MHz), Audio: Realtek ALC3234, Network: Intel I219-LM + Intel 8265 / 8275

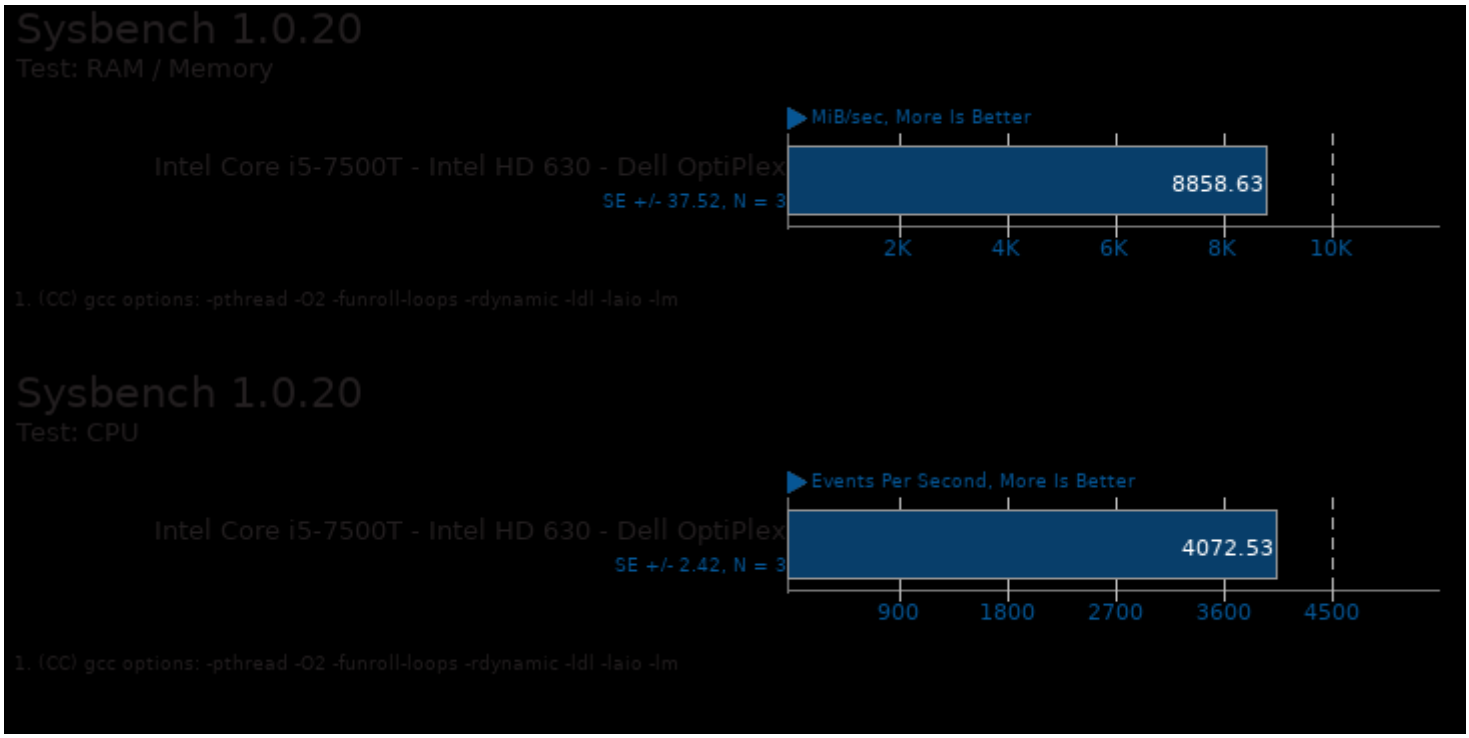
OS: Debian 11, Kernel: 5.10.0-16-amd64 (x86_64), Vulkan: 1.2.145, Compiler: GCC 10.2.1 20210110, File-System: ext4

Kernel Notes: Transparent Huge Pages: always
Compiler Notes: --build=x86_64-linux-gnu --disable-vtable-verify --disable-werror --enable-bootstrap --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++,m2 --enable-libphobos-checking=release --enable-libstdcxx-debug

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--enable-libstdcxx-time=yes --enable-link-mutex --enable-multiarch --enable-multilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none=/build/gcc-10-Km9U7s/gcc-10-10.2.1/debian/tmp-nvptx/usr,amdgcn-amdhsa=/build/gcc-10-Km9U7s/gcc-10-10.2.1/debian/tmp-gcn/usr, hsa --enable-plugin --enable-shared --enable-threads=posix --host=x86_64-linux-gnu --program-prefix=x86_64-linux-gnu- --target=x86_64-linux-gnu --with-abi=m64 --with-arch-32=i686 --with-build-config=bootstrap-lto-lean --with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multilib-list=m32,m64,mx32 --with-target-system-zlib=auto --with-tune=generic --without-cuda-driver -v
Processor Notes: Scaling Governor: intel_pstate powersave (EPP: balance_performance) - CPU Microcode: 0xea
Security Notes: itlb_multihit: KVM: Mitigation of VMX disabled + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT disabled + mds: Mitigation of Clear buffers; SMT disabled + meltdown: Mitigation of PTI + mmio_stale_data: Mitigation of Clear buffers; SMT disabled + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Retpolines IBPB: conditional IBRS_FW STIBP: disabled RSB filling + srbds: Mitigation of Microcode + tsx_async_abort: Mitigation of Clear buffers; SMT disabled

	Intel Core i5-7500T - Intel HD 630 - Dell OptiPlex
Sysbench - RAM / Memory (MiB/sec)	8859
Standard Deviation	0.7%
Sysbench - CPU (Events/sec)	4073
Standard Deviation	0.1%



This file was automatically generated via the Phoronix Test Suite benchmarking software on Thursday, 28 March 2024 15:30.