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# 0xO1-pts-opm-git-morph

AMD Ryzen 7 5700U testing with a LENOVO LNVNB161216 (GJCN20WW BIOS) and amdgpudrmfb on Ubuntu 20.04 via the Phoronix Test Suite.

## Test Systems:

### Convertible-x86_64-Ubuntu-20.04-pts-opm-git-morph-1637758780

### Convertible-x86_64-Ubuntu-20.04-pts-opm-git-morph-1637795683

Processor: AMD Ryzen 7 5700U @ 1.80GHz (8 Cores / 16 Threads), Motherboard: LENOVO LNVNB161216 (GJCN20WW BIOS), Chipset: AMD Renoir Root Complex, Memory: 16GB, Disk: 256GB HFM256GDJTNG-8310A, Graphics: amdgpudrmfb (1900/400MHz), Audio: AMD Device 1637, Network: Realtek RTL8822CE 802.11ac PCIe

OS: Ubuntu 20.04, Kernel: 5.11.0-40-generic (x86_64), Desktop: GNOME Shell 3.36.9, Display Server: X Server 1.20.11, Compiler: GCC 9.3.0, File-System: ext4, Screen Resolution: 1920x1080

Kernel Notes: Transparent Huge Pages: madvise
Compiler Notes: --build=x86_64-linux-gnu --disable-vtable-verify --disable-werror --enable-checking=release --enable-clocale=gnu --enable-default-pie

--enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++,gm2 --enable-libstdcxx-debug --enable-libstdcxx-time=yes --enable-multiarch --enable-multilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none=/build/gcc-9-HskZEa/gcc-9-9.3.0/debian/tmp-nvptx/usr,hsa --enable-plugin --enable-shared --enable-threads=posix --host=x86_64-linux-gnu --program-prefix=x86_64-linux-gnu- --target=x86_64-linux-gnu --with-abi=m64 --with-arch-32=i686 --with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multilib-list=m32,m64,mx32 --with-target-system-zlib=auto --with-tune=generic --without-cuda-driver -v
Processor Notes: Scaling Governor: acpi-cpufreq ondemand (Boost: Enabled) - CPU Microcode: 0x8608103
Security Notes: itlb_multihit: Not affected + l1tf: Not affected + mds: Not affected + meltdown: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Full AMD retpoline IBPB: conditional IBRS_FW STIBP: conditional RSB filling + srbds: Not affected + tsx_async_abort: Not affected

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