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oldram_07032021

AMD Ryzen 5 2600 Six-Core testing with a MSI B450-A PRO MAX (MS-7B86) v4.0 (M.B0 BIOS) and eVGA NVIDIA NVA0 896MB on Ubuntu 21.04 via the Phoronix Test Suite.

Test Systems:

AMD Ryzen 5 2600 Six-Core

Processor: AMD Ryzen 5 2600 Six-Core @ 3.40GHz (6 Cores / 12 Threads), Motherboard: MSI B450-A PRO MAX (MS-7B86) v4.0 (M.B0 BIOS), Chipset: AMD 17h, Memory: 16GB, Disk: 1000GB Western Digital WDS100T2B0C-00PXH0, Graphics: eVGA NVIDIA NVA0 896MB, Audio: Realtek ALC892, Monitor: HP VH240a, Network: Realtek RTL8111/8168/8411 + Qualcomm Atheros AR93xx

OS: Ubuntu 21.04, Kernel: 5.11.0-22-generic (x86_64), Desktop: GNOME Shell 3.38.4, Display Server: X Server + Wayland, Display Driver: nouveau, OpenGL: 3.3 Mesa 21.0.1, Vulkan: 1.0.2, Compiler: GCC 10.3.0, File-System: ext4, Screen Resolution: 1920x1080

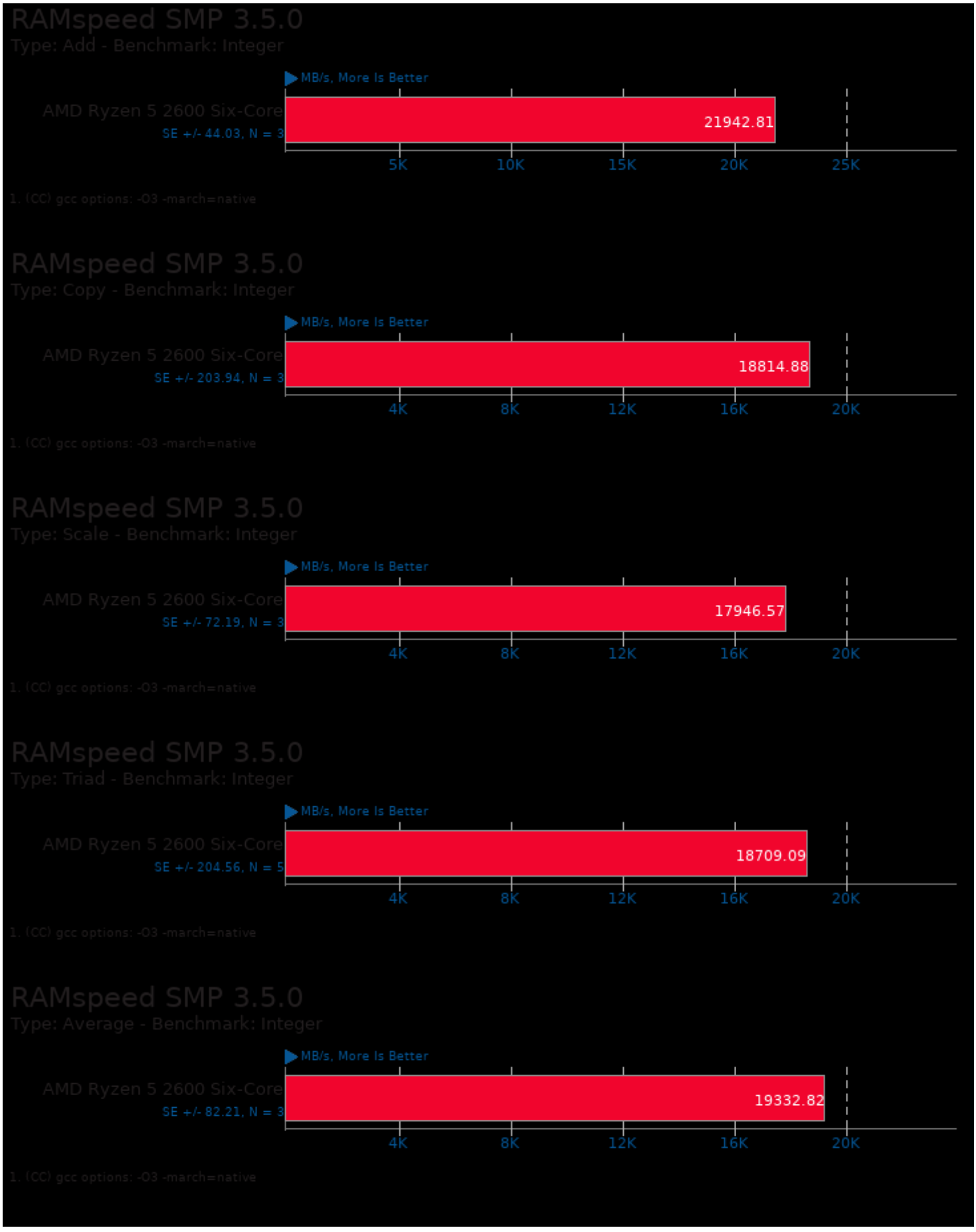
Kernel Notes: Transparent Huge Pages: madvise
Compiler Notes: --build=x86_64-linux-gnu --disable-vtable-verify --disable-werror --enable-bootstrap --enable-checking=release --enable-clocale=gnu --enable-default-pie

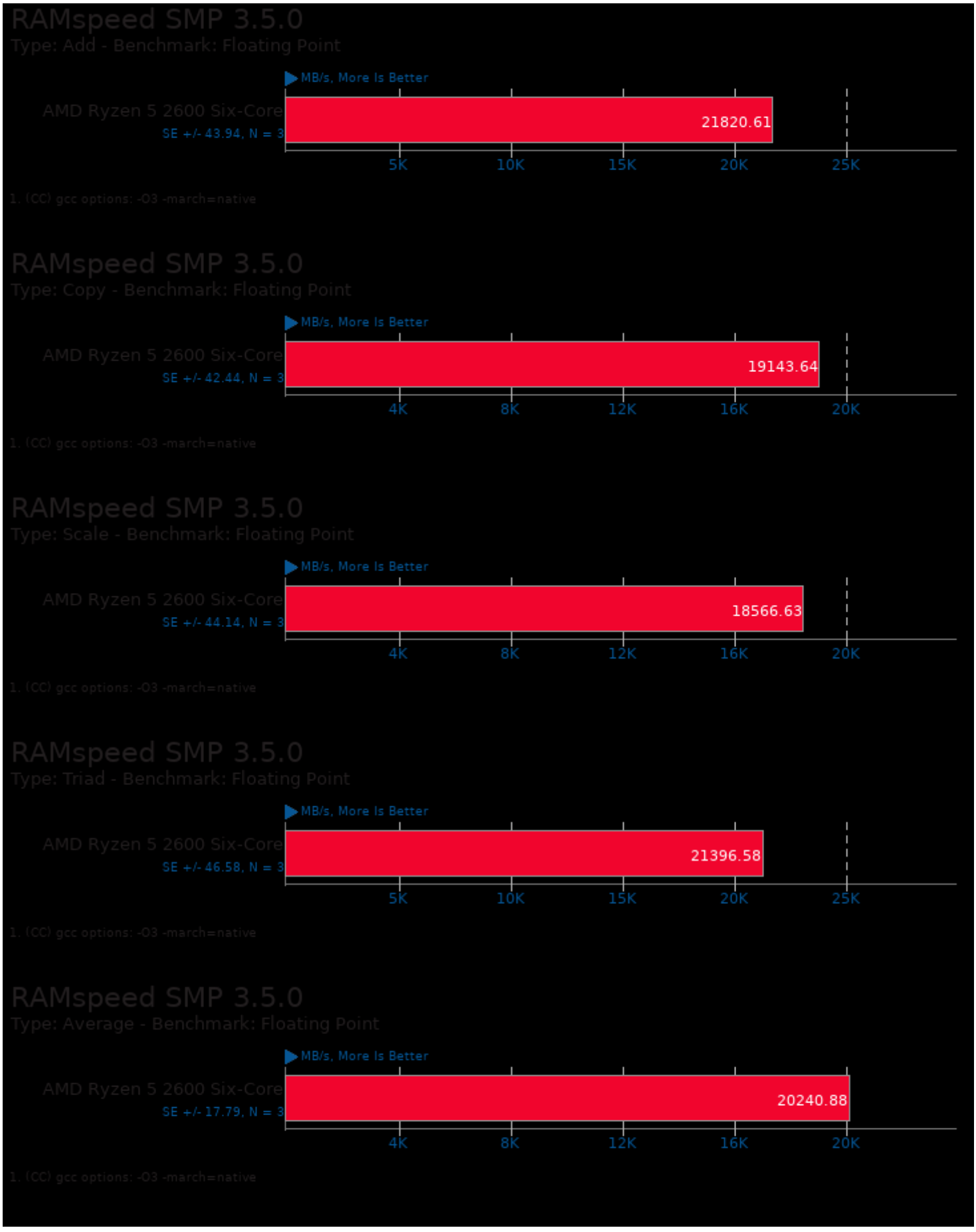
--enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++,m2 --enable-libphobos-checking=release --enable-libstdcxx-debug --enable-libstdcxx-time=yes --enable-link-mutex --enable-multiarch --enable-multilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none=/build/gcc-10-gDeRY6/gcc-10-10.3.0/debian/tmp-nvptx/usr,amdgcn-amdhsa=/build/gcc-10-gDeRY6/gcc-10-10.3.0/debian/tmp-gcn/usr, hsa --enable-plugin --enable-shared --enable-threads=posix --host=x86_64-linux-gnu --program-prefix=x86_64-linux-gnu- --target=x86_64-linux-gnu --with-abi=m64 --with-arch-32=i686 --with-build-config=bootstrap-lto-lean --with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multilib-list=m32,m64,mx32 --with-target-system-zlib=auto --with-tune=generic --without-cuda-driver -v
Processor Notes: Scaling Governor: acpi-cpufreq schedutil (Boost: Enabled) - CPU Microcode: 0x800820d
Security Notes: itlb_multihit: Not affected + l1tf: Not affected + mds: Not affected + meltdown: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Full AMD retpoline IBPB: conditional STIBP: disabled RSB filling + srbds: Not affected + tsx_async_abort: Not affected

	AMD Ryzen 5 2600 Six-Core
RAMspeed SMP - Add - Integer (MB/s)	21943
Standard Deviation	0.3%
RAMspeed SMP - Copy - Integer (MB/s)	18815
Standard Deviation	1.9%
RAMspeed SMP - Scale - Integer (MB/s)	17947
Standard Deviation	0.7%
RAMspeed SMP - Triad - Integer (MB/s)	18709
Standard Deviation	2.4%
RAMspeed SMP - Average - Integer (MB/s)	19333
Standard Deviation	0.7%
RAMspeed SMP - Add - Floating Point (MB/s)	21821
Standard Deviation	0.3%
RAMspeed SMP - Copy - Floating Point (MB/s)	19144
Standard Deviation	0.4%
RAMspeed SMP - Scale - Floating Point (MB/s)	18567
Standard Deviation	0.4%
RAMspeed SMP - Triad - Floating Point (MB/s)	21397
Standard Deviation	0.4%
RAMspeed SMP - Average - Floating Point (MB/s)	20241
Standard Deviation	0.2%
Stream - Copy (MB/s)	27845
Standard Deviation	0%
Stream - Scale (MB/s)	19012
Standard Deviation	0%
Stream - Triad (MB/s)	21709
Standard Deviation	0%
Stream - Add (MB/s)	21800
Standard Deviation	0%
Tinymembench - Standard Memcpy (MB/s)	12479
Standard Deviation	0%
Tinymembench - Standard Memset (MB/s)	9837
Standard Deviation	0.2%
MBW - Memory Copy - 1024 MiB (MiB/s)	12246
Standard Deviation	0.5%
MBW - M.C.F.B.S - 1024 MiB (MiB/s)	7065
Standard Deviation	1.6%
t-test1 - 1 (sec)	45.884
Standard Deviation	0.3%
t-test1 - 2 (sec)	15.743
Standard Deviation	2.5%

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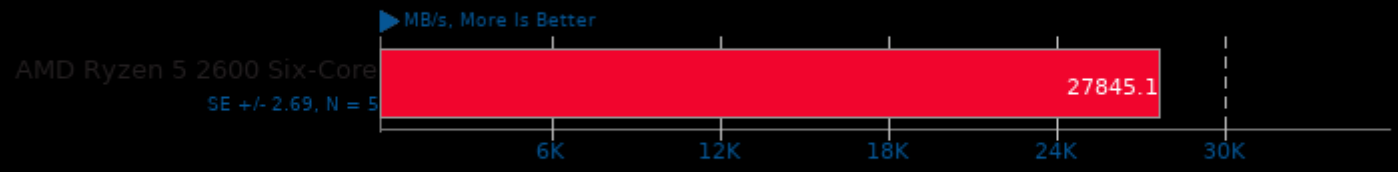
CacheBench - Read Cache (MB/s)	2977
Standard Deviation	0%
CacheBench - Write Cache (MB/s)	26771
Standard Deviation	0.1%


RAMspeed SMP 3.5.0
Type: Add - Benchmark: Integer
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 44.03, N = 3
21942.81
5K
10K
15K
20K
25K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Copy - Benchmark: Integer
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 203.94, N = 3
18814.88
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Scale - Benchmark: Integer
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 72.19, N = 3
17946.57
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Triad - Benchmark: Integer
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 204.56, N = 5
18709.09
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Average - Benchmark: Integer
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 82.21, N = 3
19332.82
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native


RAMspeed SMP 3.5.0
Type: Add - Benchmark: Floating Point
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 43.94, N = 3
21820.61
5K
10K
15K
20K
25K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Copy - Benchmark: Floating Point
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 42.44, N = 3
19143.64
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Scale - Benchmark: Floating Point
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 44.14, N = 3
18566.63
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Triad - Benchmark: Floating Point
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 46.58, N = 3
21396.58
5K
10K
15K
20K
25K
1. (CC) gcc options: -O3 -march=native
RAMspeed SMP 3.5.0
Type: Average - Benchmark: Floating Point
MB/s, More Is Better
AMD Ryzen 5 2600 Six-Core
SE +/- 17.79, N = 3
20240.88
4K
8K
12K
16K
20K
1. (CC) gcc options: -O3 -march=native

Stream 2013-01-17

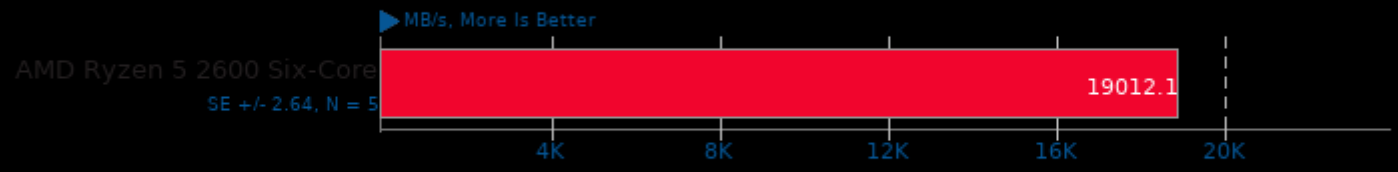
Type: Copy



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

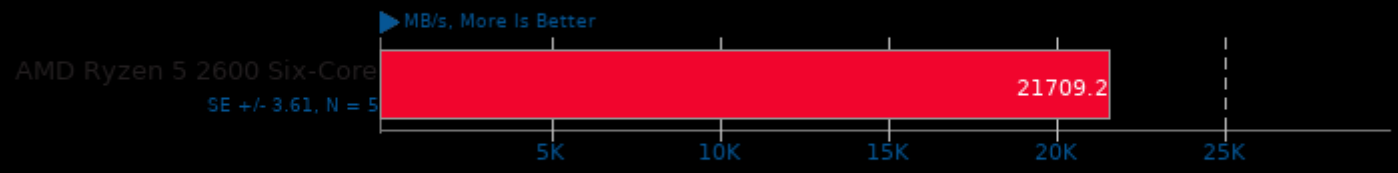
Type: Scale



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

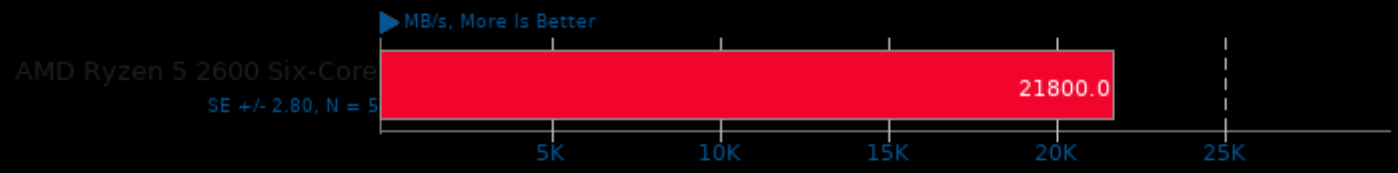
Type: Triad



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

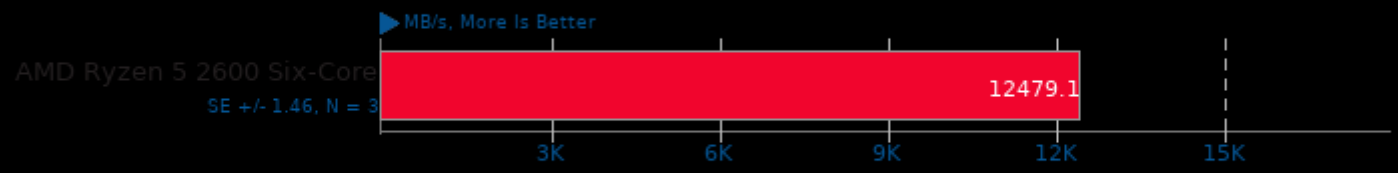
Type: Add



1. (CC) gcc options: -O3 -march=native -fopenmp

Tinymembench 2018-05-28

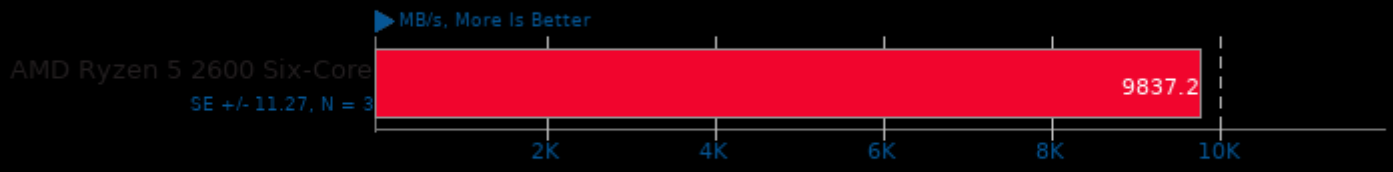
Standard Memcpy



1. (CC) gcc options: -O2 -lm

Tinymembench 2018-05-28

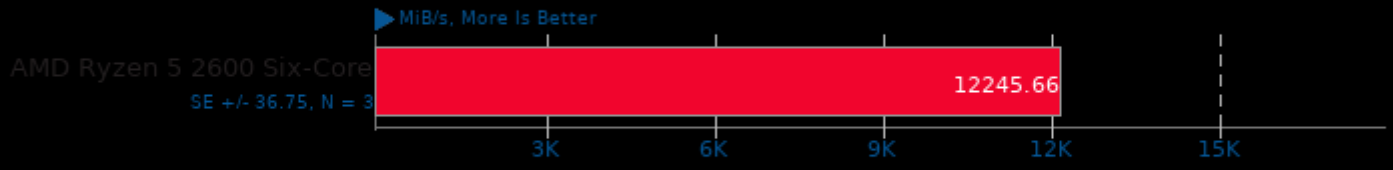
Standard Memset



1. (CC) gcc options: -O2 -lm

MBW 2018-09-08

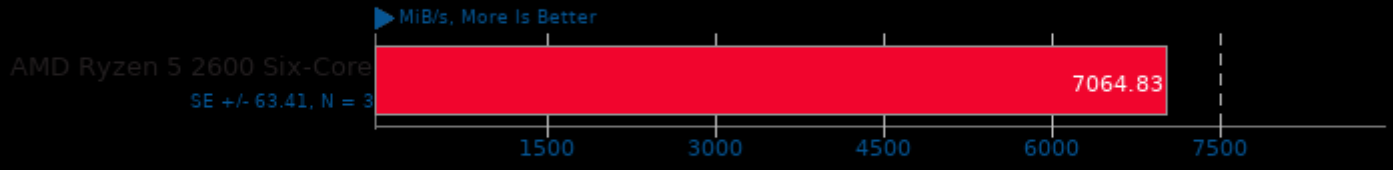
Test: Memory Copy - Array Size: 1024 MiB



1. (CC) gcc options: -O3 -march=native

MBW 2018-09-08

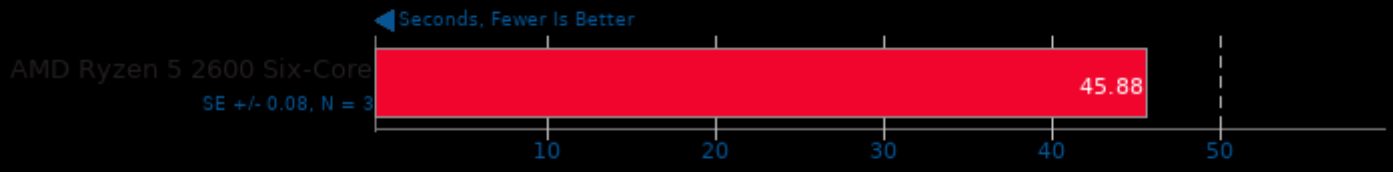
Test: Memory Copy, Fixed Block Size - Array Size: 1024 MiB



1. (CC) gcc options: -O3 -march=native

t-test1 2017-01-13

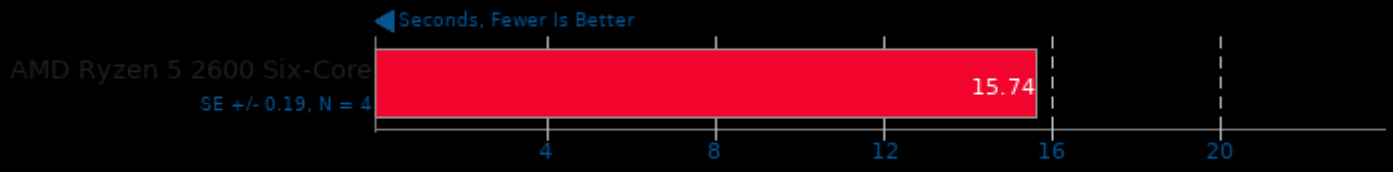
Threads: 1



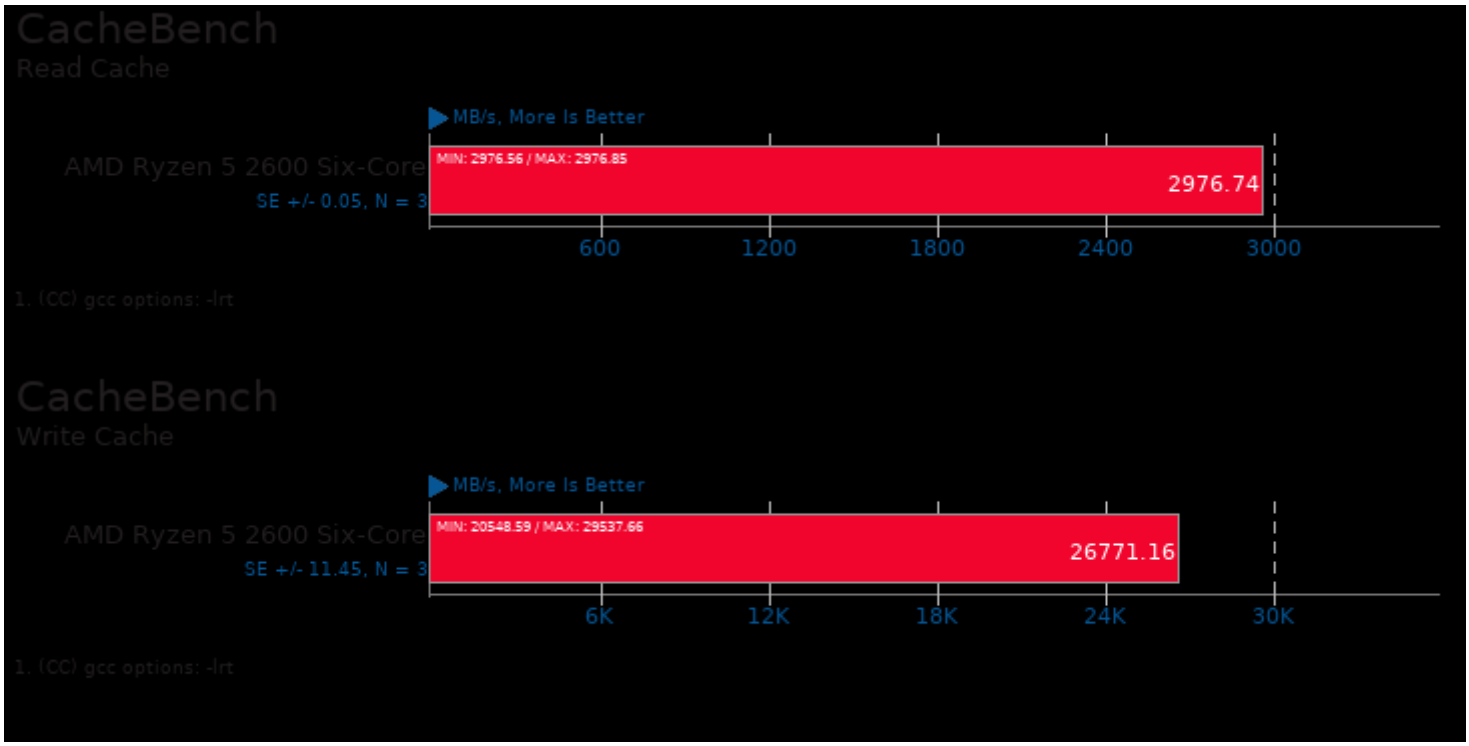
1. (CC) gcc options: -pthread

t-test1 2017-01-13

Threads: 2



1. (CC) gcc options: -pthread



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