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Scimark First

AMD Ryzen 5 3600 6-Core testing with a ASUS ROG CROSSHAIR VIII HERO (3204 BIOS) and XFX AMD Radeon RX 470/480/570/570X/580/580X/590 4GB on Arch rolling via the Phoronix Test Suite.

Test Systems:

Scimark First Ryzen 3600

Processor: AMD Ryzen 5 3600 6-Core @ 4.00GHz (6 Cores / 12 Threads), Motherboard: ASUS ROG CROSSHAIR VIII HERO (3204 BIOS), Chipset: AMD Starship/Matisse, Memory: 16GB, Disk: Samsung SSD 960 EVO 250GB + 3001GB Seagate ST3000DM008-2DM1 + 2000GB Seagate ST2000DM006-2DM1 + 32GB Mass-Storage, Graphics: XFX AMD Radeon RX 470/480/570/570X/580/580X/590 4GB (1375/1850MHz), Audio: AMD Ellesmere HDMI Audio, Monitor: PX329, Network: Realtek RTL8125 2.5GbE + Intel I211

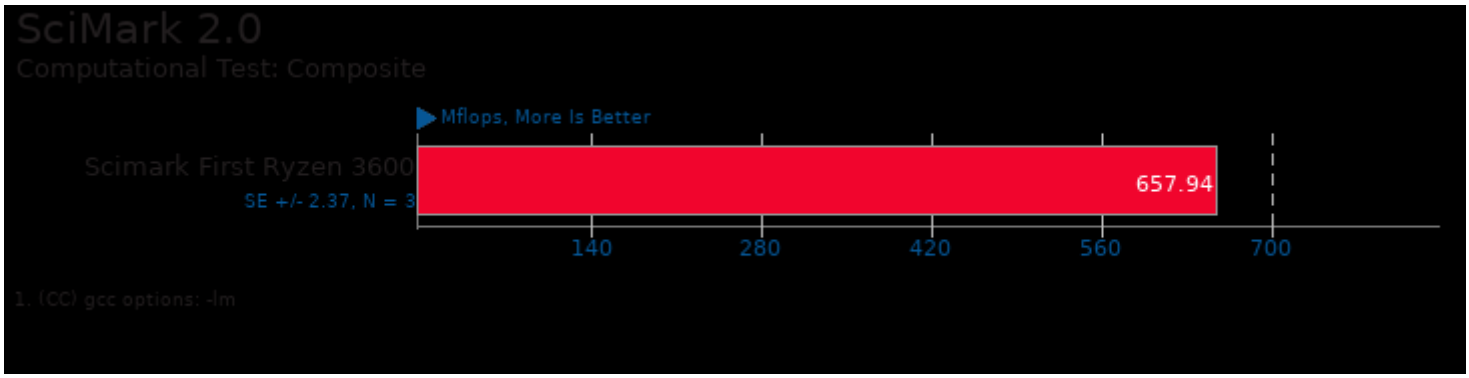
OS: Arch rolling, Kernel: 5.10.16-zen1-1-zen (x86_64), Desktop: KDE Plasma 5.21.0, Display Server: X Server 1.20.10, Display Driver: xfx, OpenGL: 4.6 Mesa 20.3.4 (LLVM 11.1.0), Compiler: GCC 10.2.0 + Clang 11.1.0, File-System: ext4, Screen Resolution: 2560x1440

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Kernel Notes: amdgpu.ppfeaturemask=0xfffd7fff - Transparent Huge Pages: always
Environment Notes: RADV_PERFTEST=aco
Compiler Notes: --disable-libssp --disable-libstdcxx-pch --disable-libunwind-exceptions --disable-werror --enable-__cxa_atexit --enable-cet=auto --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-default-ssp --enable-gnu-indirect-function --enable-gnu-unique-object --enable-install-libiberty --enable-languages=c,c++,ada,fortran,go,lto,objc,obj-c++,d --enable-lto --enable-multilib --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --with-isl --with-linker-hash-style=gnu
Processor Notes: Scaling Governor: acpi-cpufreq schedutil (Boost: Disabled) - CPU Microcode: 0x8701021
Security Notes: itlb_multihit: Not affected + l1tf: Not affected + mds: Not affected + meltdown: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Full AMD retpoline IBPB: conditional STIBP: conditional RSB filling + srbds: Not affected + tsx_async_abort: Not affected

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SciMark - Composite (Mflops)	657.94
Standard Deviation	0.6%

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This file was automatically generated via the Phoronix Test Suite benchmarking software on Friday, 29 March 2024 07:23.