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celcorev3

Intel Celeron J4105 testing with a ASRock J4105-ITX (P1.00 BIOS) and Intel UHD 605 on Ubuntu 20.04 via the Phoronix Test Suite.

Test Systems:

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Processor: Intel Celeron J4105 @ 2.50GHz (4 Cores), Motherboard: ASRock J4105-ITX (P1.00 BIOS), Chipset: Intel Gemini Lake, Memory: 8GB, Disk: 240GB Patriot Burst, Graphics: Intel UHD 605 (750MHz), Audio: Realtek ALC892, Monitor: SMB2230H, Network: Realtek RTL8111/8168/8411

OS: Ubuntu 20.04, Kernel: 5.4.0-65-generic (x86_64), Compiler: GCC 9.3.0, File-System: ext4, Screen Resolution: 1920x1080

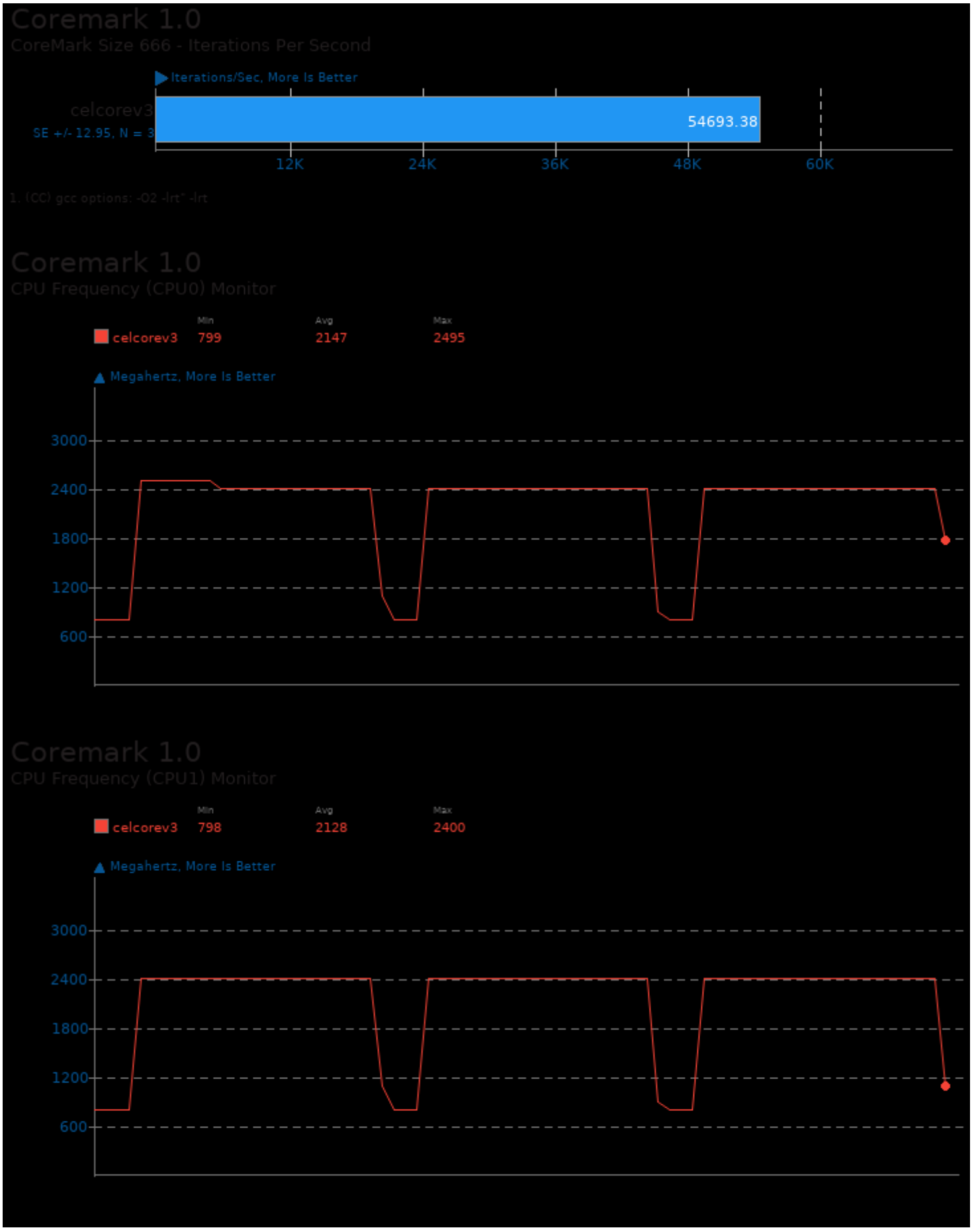
Kernel Notes: Transparent Huge Pages: madvise
Compiler Notes: --build=x86_64-linux-gnu --disable-vtable-verify --disable-werror --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++,gm2 --enable-libstdcxx-debug --enable-libstdcxx-time=yes --enable-multiarch --enable-multilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none=/build/gcc-9-HskZEa/gcc-9-9.3.0/debian/tmp-nvptx/usr,hsa --enable-plugin --enable-shared --enable-threads=posix --host=x86_64-linux-gnu --program-prefix=x86_64-linux-gnu- --target=x86_64-linux-gnu --with-abi=m64 --with-arch-32=i686

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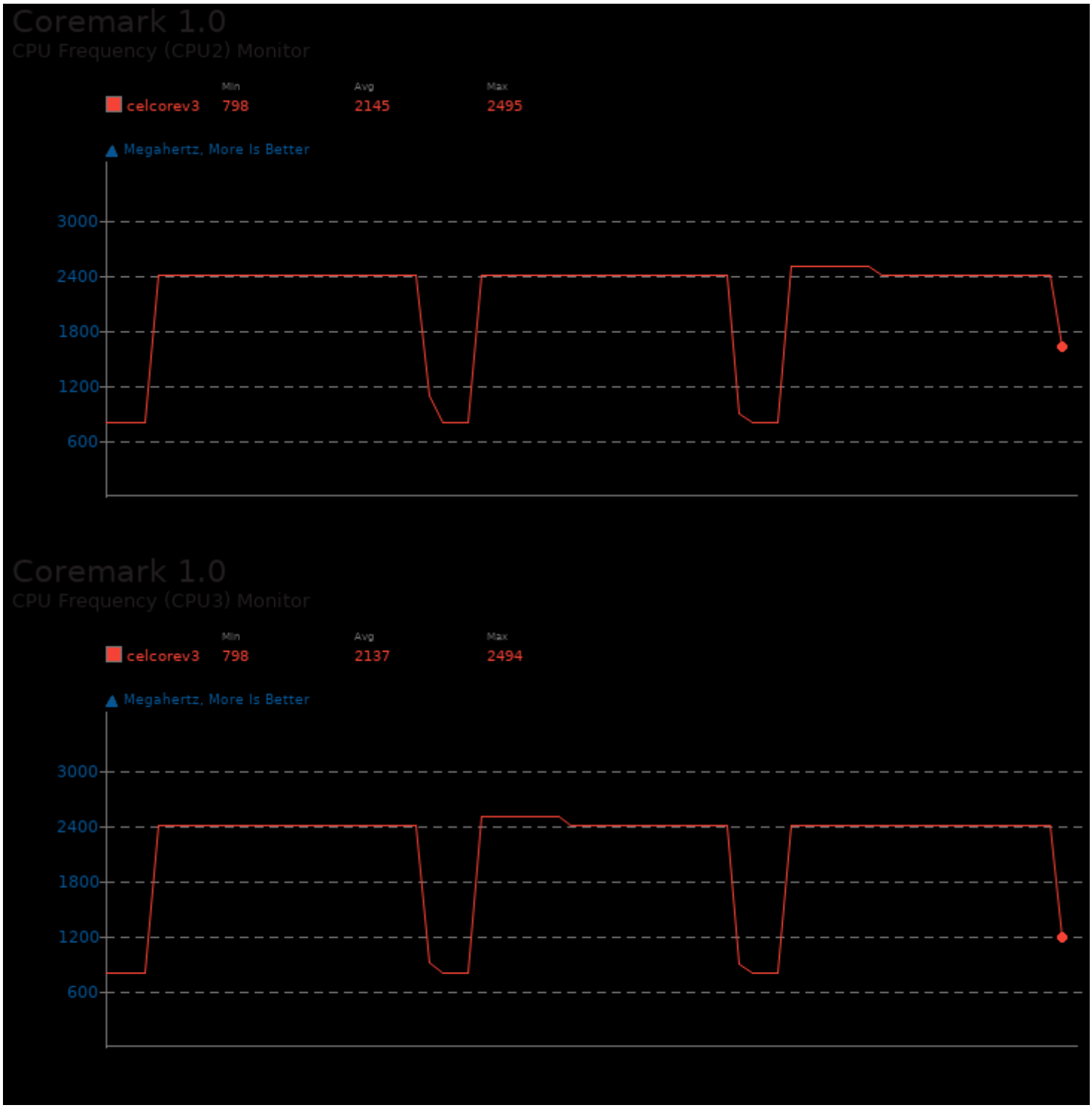
--with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multilib-list=m32,m64,mx32 --with-target-system-zlib=auto --with-tune=generic --without-cuda-driver -v
Processor Notes: Scaling Governor: intel_pstate powersave - CPU Microcode: 0x32 - Thermald 1.9.1
Security Notes: itlb_multihit: Not affected + l1tf: Not affected + mds: Not affected + meltdown: Mitigation of PTI + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Enhanced IBRS IBPB: conditional RSB filling + srbds: Not affected + tsx_async_abort: Not affected

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Coremark - CoreMark Size 666 - I.P.S (Iterations/Sec)	54693
Standard Deviation	0%

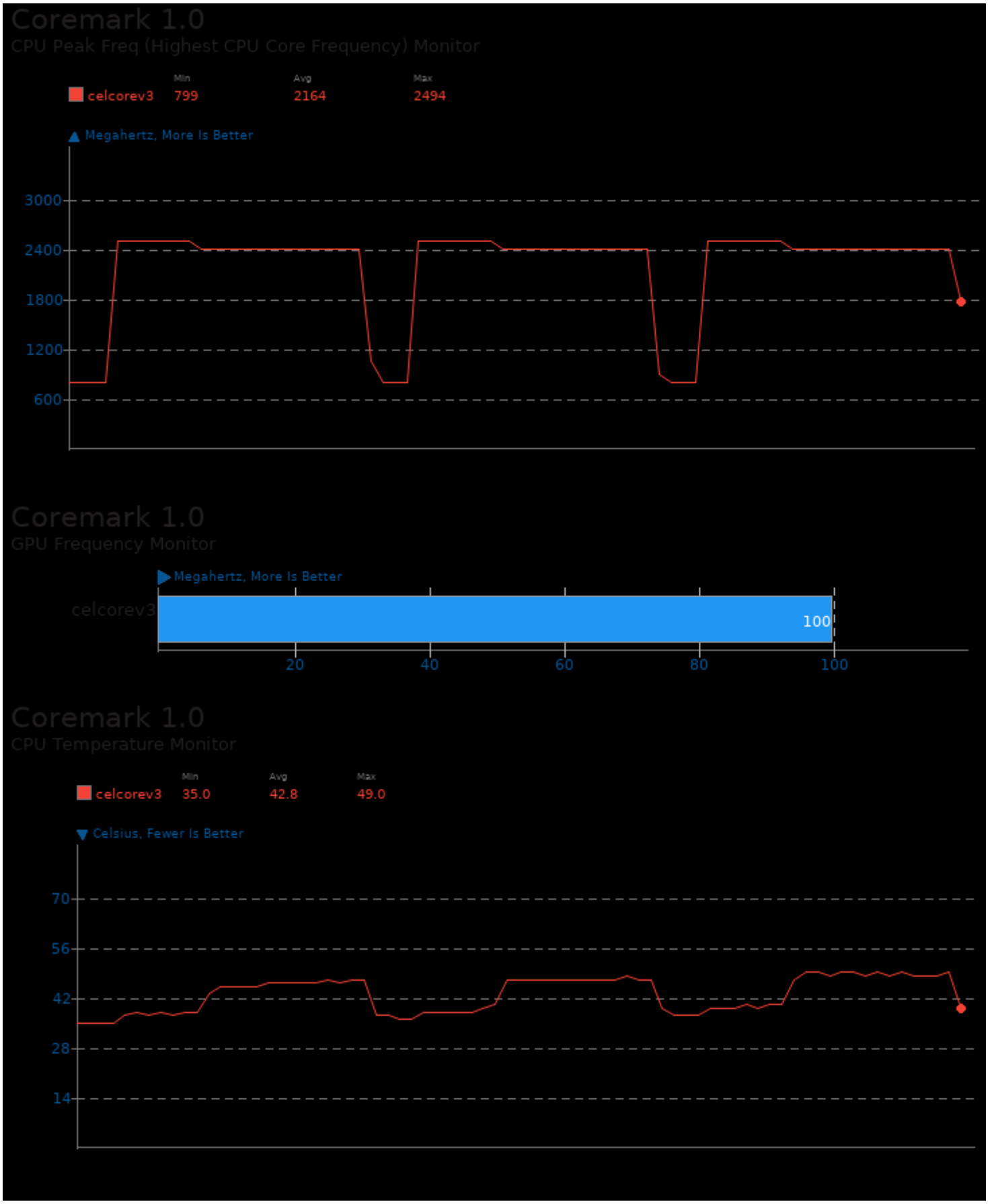
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Coremark 1.0
CoreMark Size 666 - Iterations Per Second
Iterations/Sec, More Is Better
celcorev3
SE +/- 12.95, N = 3
54693.38
12K
24K
36K
48K
60K
1. (CC) gcc options: -O2 -lrt" -lrt
Coremark 1.0
CPU Frequency (CPU0) Monitor
Min
Avg
Max
celcorev3
799
2147
2495
Megahertz, More Is Better
3000
2400
1800
1200
600
Coremark 1.0
CPU Frequency (CPU1) Monitor
Min
Avg
Max
celcorev3
798
2128
2400
Megahertz, More Is Better
3000
2400
1800
1200
600

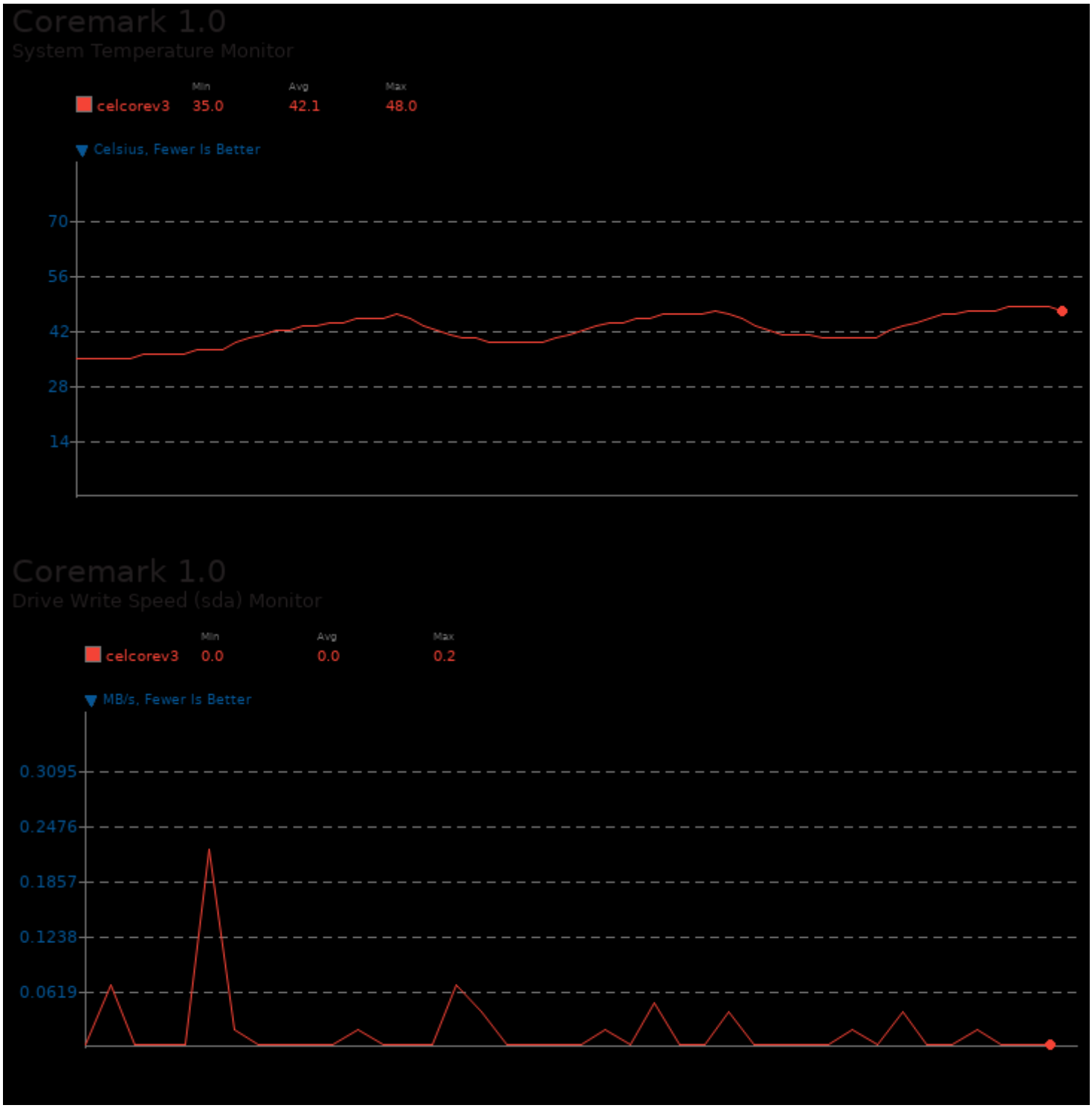
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Coremark 1.0
CPU Frequency (CPU2) Monitor
Min
Avg
Max
celcorev3
798
2145
2495
Megahertz, More Is Better
3000
2400
1800
1200
600
Coremark 1.0
CPU Frequency (CPU3) Monitor
Min
Avg
Max
celcorev3
798
2137
2494
Megahertz, More Is Better
3000
2400
1800
1200
600

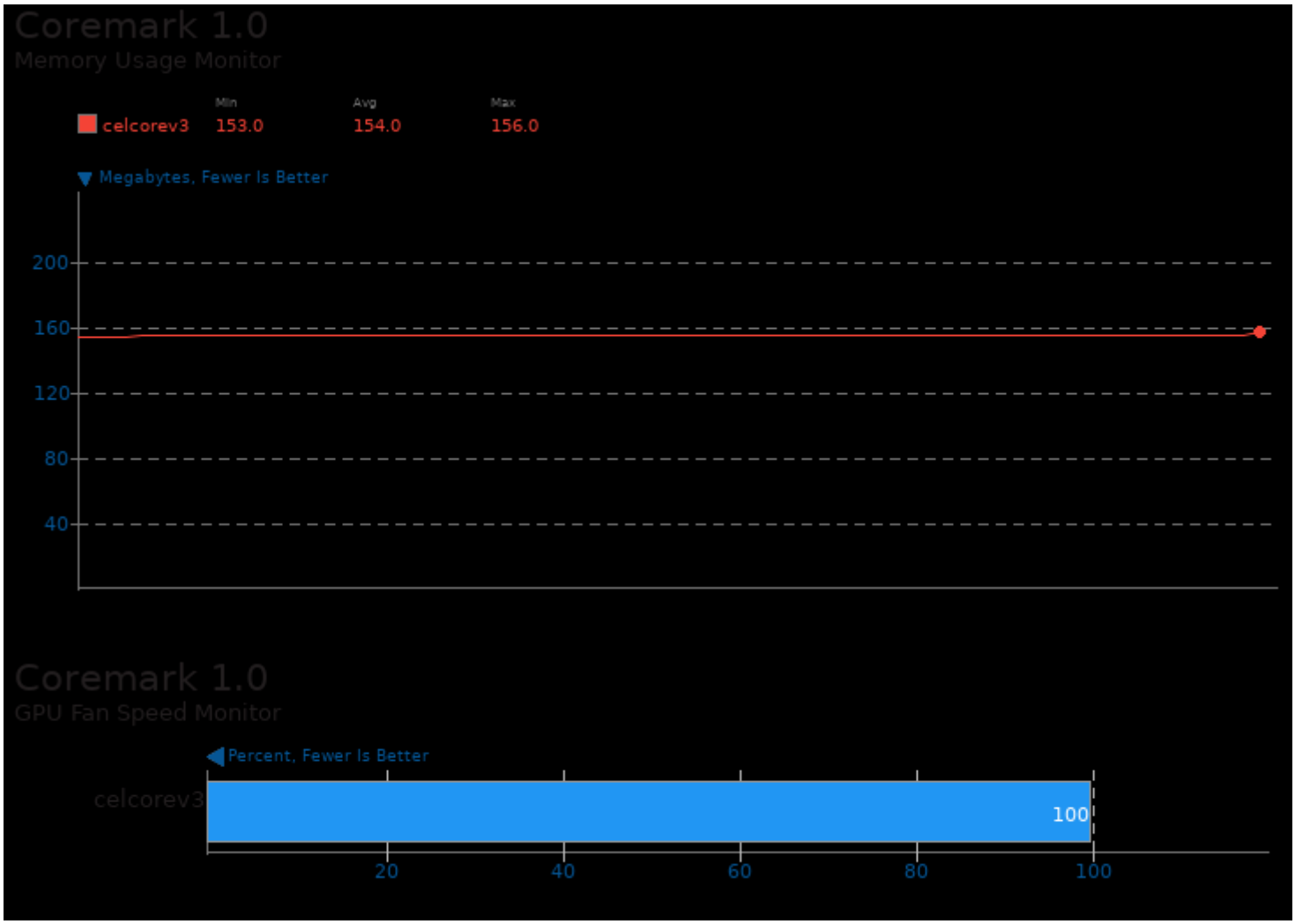
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Coremark 1.0
CPU Peak Freq (Highest CPU Core Frequency) Monitor
Min
Avg
Max
celcorev3
799
2164
2494
Megahertz, More Is Better
3000
2400
1800
1200
600
Coremark 1.0
GPU Frequency Monitor
Megahertz, More Is Better
celcorev3
100
20
40
60
80
100
Coremark 1.0
CPU Temperature Monitor
Min
Avg
Max
celcorev3
35.0
42.8
49.0
Celsius, Fewer Is Better
70
56
42
28
14

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Coremark 1.0
System Temperature Monitor
Min
Avg
Max
celcorev3
35.0
42.1
48.0
Celsius, Fewer Is Better
70
56
42
28
14
Coremark 1.0
Drive Write Speed (sda) Monitor
Min
Avg
Max
celcorev3
0.0
0.0
0.2
MB/s, Fewer Is Better
0.3095
0.2476
0.1857
0.1238
0.0619

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