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initial-memory-run

Intel Core i7-4790K testing with a ASRock Z97 Pro4 (P2.50 BIOS) and eVGA NVIDIA GeForce GTX 970 4GB on Arch rolling via the Phoronix Test Suite.

Test Systems:

initial memory run

Processor: Intel Core i7-4790K @ 4.40GHz (4 Cores / 8 Threads), Motherboard: ASRock Z97 Pro4 (P2.50 BIOS), Chipset: Intel 4th Gen Core DRAM, Memory: 32GB, Disk: 1000GB Western Digital WDS100T3XHC-00SJG0 + 1000GB Western Digital WDS100T2B0A + 250GB Samsung SSD 850, Graphics: eVGA NVIDIA GeForce GTX 970 4GB (1227/3505MHz), Audio: Realtek ALC892, Monitor: K272HUL, Network: Intel I218-V + Mellanox MT27500

OS: Arch rolling, Kernel: 5.9.10-arch1-1 (x86_64), Desktop: Xfce 4.14, Display Server: X Server 1.20.9, Display Driver: NVIDIA 455.45.01, OpenGL: 4.6.0, Vulkan: 1.2.142, Compiler: GCC 10.2.0 + Clang 11.0.0Target: + LLVM 11.0.0, File-System: ext4, Screen Resolution: 5120x1440

Compiler Notes: --disable-libssp --disable-libstdcxx-pch --disable-libunwind-exceptions --disable-werror --enable-__cxa_atexit --enable-cet=auto --enable-checking=release --enable-clocale-gnu --enable-default-pie --enable-default-ssp --enable-gnu-indirect-function --enable-gnu-unique-object --enable-install-liberty

--enable-languages=c,c++,ada,fortran,go,ito,objc,obj-c++,d --enable-lto --enable-multilib --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --with-isl --with-linker-hash-style-gnu

Processor Notes: Scaling Governor: intel_cpufreq schedutil - CPU Microcode: 0x28

Security Notes: i1lb_multithit: KVM: Mitigation of VMX disabled + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT vulnerable + mds: Mitigation of Clear buffers; SMT vulnerable + meltdown: Mitigation of PTI + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of Full generic retpoline IBPB: conditional IBRS_FW STIBP: conditional RSB filling + srbd: Mitigation of Microcode + tsx_async_abort: Not affected

initial memory run

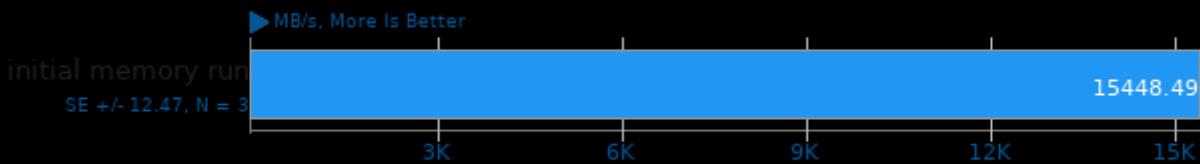
RAMspeed SMP - Add - Integer (MB/s)	15448
Standard Deviation	0.1%
RAMspeed SMP - Copy - Integer (MB/s)	13879
Standard Deviation	0.2%
RAMspeed SMP - Scale - Integer (MB/s)	13856
Standard Deviation	0.3%
RAMspeed SMP - Triad - Integer (MB/s)	15503
Standard Deviation	0.2%
RAMspeed SMP - Average - Integer (MB/s)	14645
Standard Deviation	0.7%
RAMspeed SMP - Add - Floating Point (MB/s)	15430
Standard Deviation	0.2%
RAMspeed SMP - Copy - Floating Point (MB/s)	13893
Standard Deviation	0.1%
RAMspeed SMP - Scale - Floating Point (MB/s)	13926
Standard Deviation	0.4%
RAMspeed SMP - Triad - Floating Point (MB/s)	15550
Standard Deviation	0.1%
RAMspeed SMP - Average - Floating Point (MB/s)	14700
Standard Deviation	0.1%
Stream - Copy (MB/s)	19290
Standard Deviation	0.3%
Stream - Scale (MB/s)	14458
Standard Deviation	0.1%
Stream - Triad (MB/s)	16172
Standard Deviation	0.1%
Stream - Add (MB/s)	16175
Standard Deviation	0.2%
Tinymembench - Standard Memcpy (MB/s)	11120
Standard Deviation	0.4%
Tinymembench - Standard Memset (MB/s)	27910
Standard Deviation	1.3%
MBW - Memory Copy - 1024 MiB (MiB/s)	10074
Standard Deviation	1.1%
MBW - M.C.F.B.S - 1024 MiB (MiB/s)	7667
Standard Deviation	2.9%
t-test1 - 1 (sec)	24.468
Standard Deviation	2.9%
t-test1 - 2 (sec)	7.785
Standard Deviation	0.3%
CacheBench - Read Cache (MB/s)	6509
Standard Deviation	1%

CacheBench - Write Cache (MB/s) 24054

Standard Deviation 0.6%

RAMspeed SMP 3.5.0

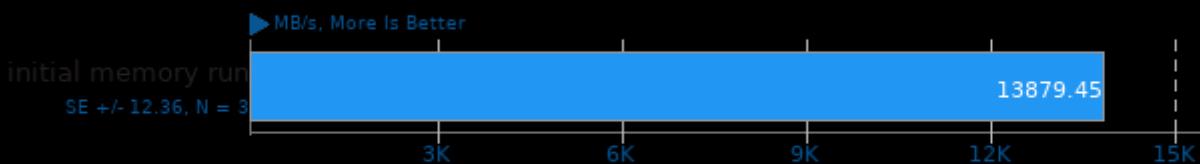
Type: Add - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

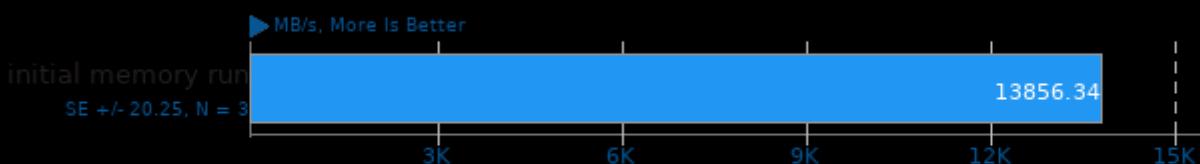
Type: Copy - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

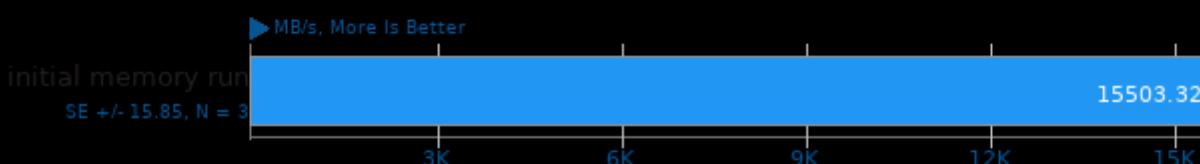
Type: Scale - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

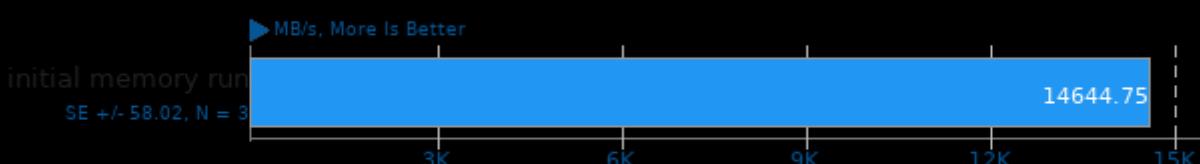
Type: Triad - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

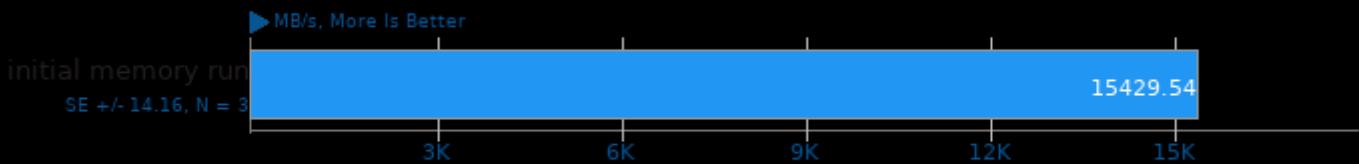
Type: Average - Benchmark: Integer



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

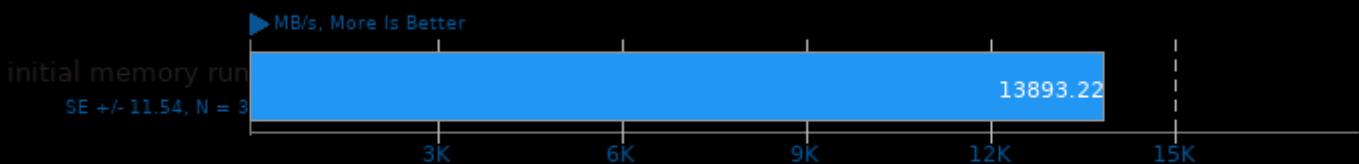
Type: Add - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

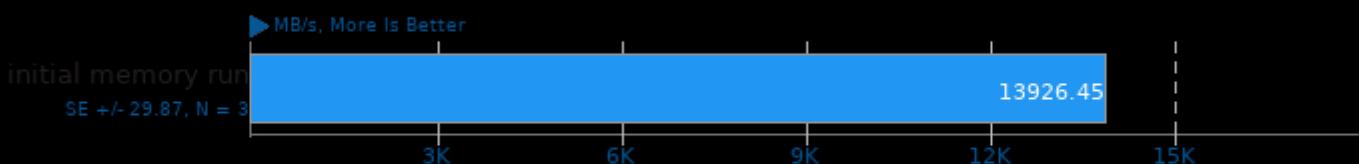
Type: Copy - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

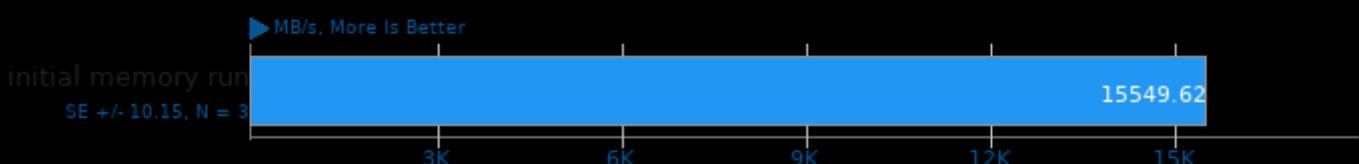
Type: Scale - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

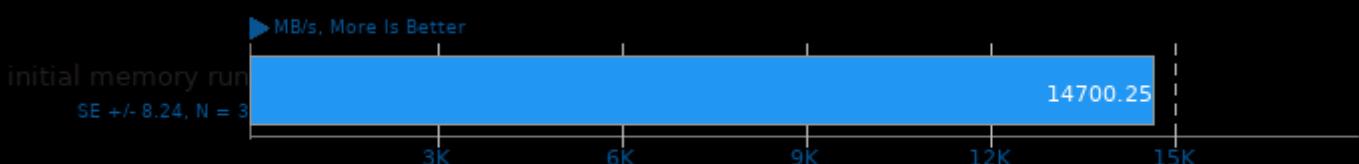
Type: Triad - Benchmark: Floating Point



1. (CC) gcc options: -O3 -march=native

RAMspeed SMP 3.5.0

Type: Average - Benchmark: Floating Point

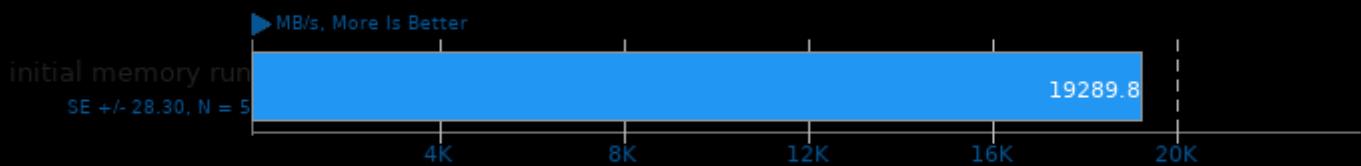


1. (CC) gcc options: -O3 -march=native

initial-memory-run

Stream 2013-01-17

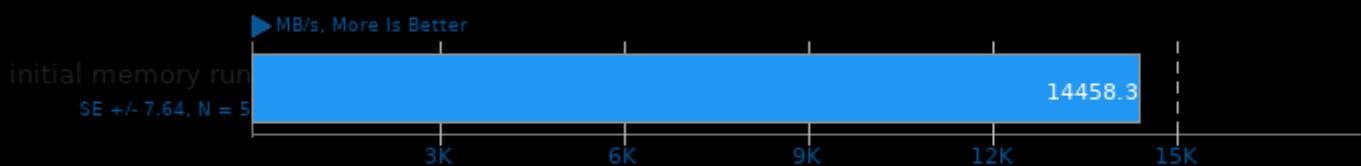
Type: Copy



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

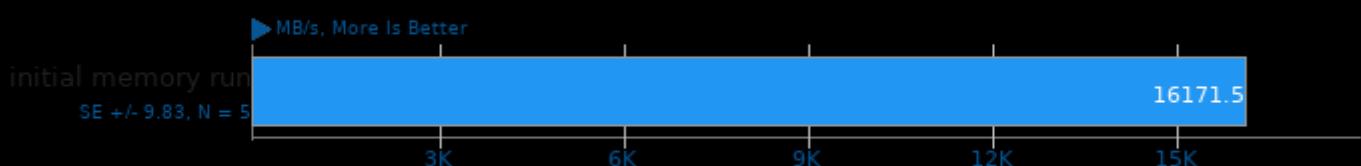
Type: Scale



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

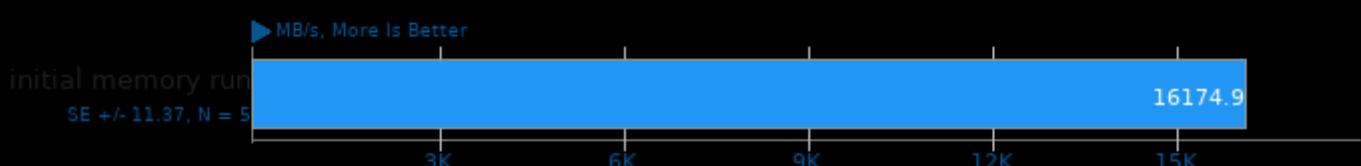
Type: Triad



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

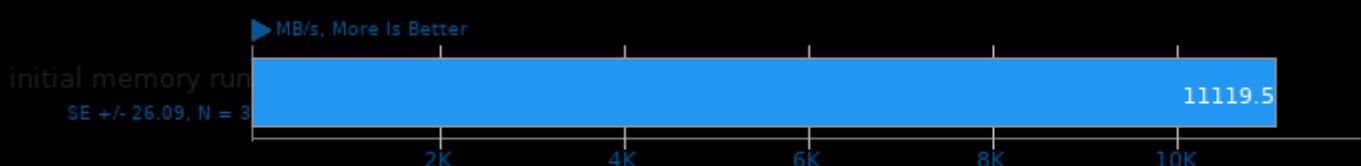
Type: Add



1. (CC) gcc options: -O3 -march=native -fopenmp

Tinymembench 2018-05-28

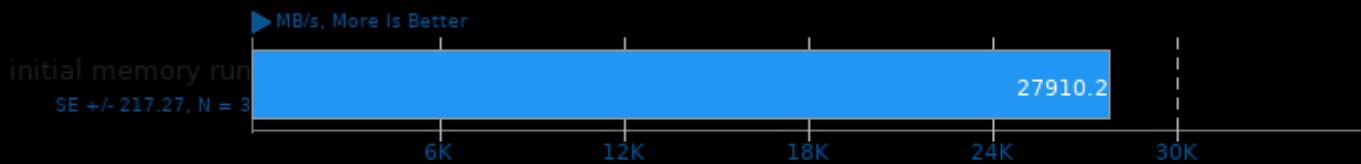
Standard Memcpy



1. (CC) gcc options: -O2 -lm

Tinymembench 2018-05-28

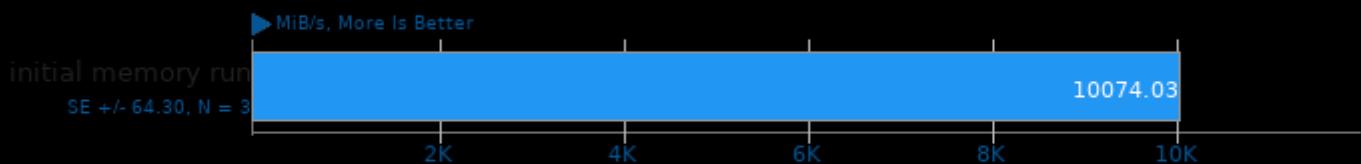
Standard Memset



1. (CC) gcc options: -O2 -lm

MBW 2018-09-08

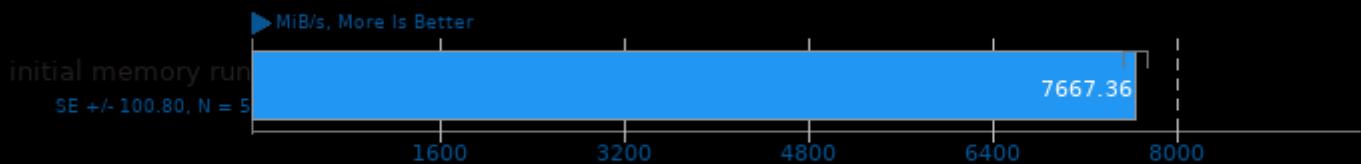
Test: Memory Copy - Array Size: 1024 MiB



1. (CC) gcc options: -O3 -march=native

MBW 2018-09-08

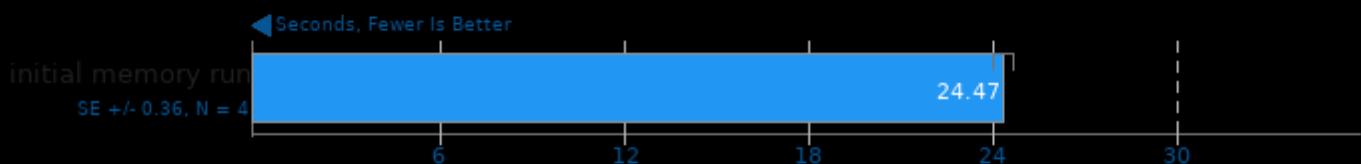
Test: Memory Copy, Fixed Block Size - Array Size: 1024 MiB



1. (CC) gcc options: -O3 -march=native

t-test1 2017-01-13

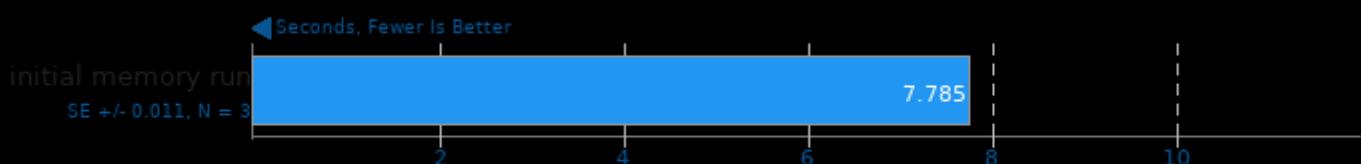
Threads: 1



1. (CC) gcc options: -pthread

t-test1 2017-01-13

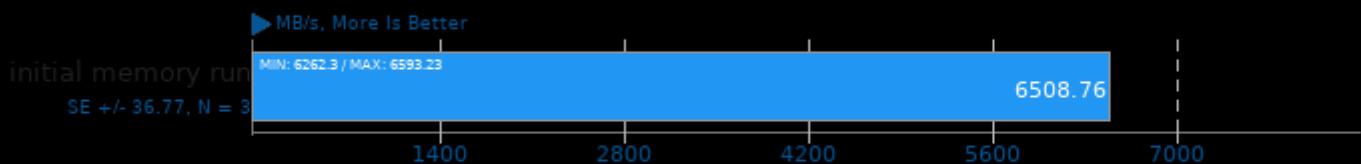
Threads: 2



1. (CC) gcc options: -pthread

CacheBench

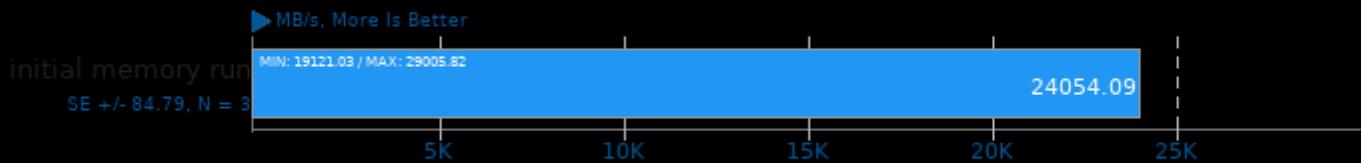
Read Cache



1. (CC) gcc options: -lrt

CacheBench

Write Cache



1. (CC) gcc options: -lrt

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