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memorysuite1

Intel Celeron J4105 testing with a HARDKERNEL ODROID-H2 v1.0 (5.13 BIOS) and inteldrmfb on Ubuntu 19.04 via the Phoronix Test Suite.

Test Systems:

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Processor: Intel Celeron J4105 @ 2.50GHz (4 Cores), Motherboard: HARDKERNEL ODROID-H2 v1.0 (5.13 BIOS), Chipset: Intel Device 31f0, Memory: 32768MB, Disk: 128GB SAMSUNG MZVLW128HEGR-000H1 + 2GB Mass Storage, Graphics: inteldrmfb (750MHz), Audio: Realtek ALC662 rev1, Monitor: VG248, Network: 2 x Realtek RTL8111/8168/8411

OS: Ubuntu 19.04, Kernel: 5.0.0-13-generic (x86_64), Desktop: GNOME Shell 3.32.1, Display Server: X Server 1.20.4, Display Driver: modesetting 1.20.4, Compiler: GCC 8.3.0, File-System: ext4, Screen Resolution: 1920x1080

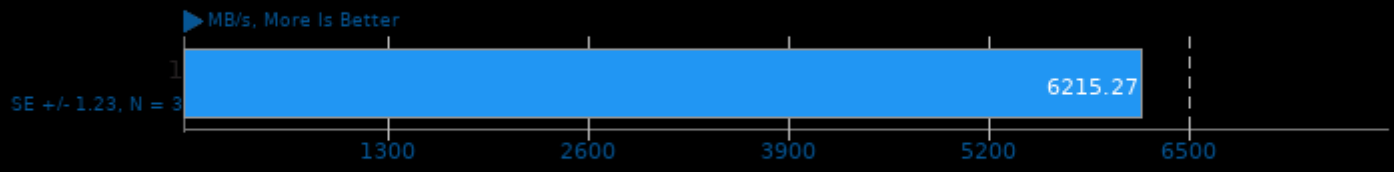
Compiler Notes: --build=x86_64-linux-gnu --disable-vtable-verify --disable-werror --enable-bootstrap --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-gnu-unique-object --enable-languages=c,ada,c++,go,brig,d,fortran,objc,obj-c++ --enable-libmpx --enable-libstdcxx-debug --enable-libstdcxx-time=yes --enable-multiarch --enable-multilib --enable-nls --enable-objc-gc=auto --enable-offload-targets=nvptx-none --enable-plugin --enable-shared --enable-threads=posix

memorysuite1

--host=x86_64-linux-gnu --program-prefix=x86_64-linux-gnu- --target=x86_64-linux-gnu --with-abi=m64 --with-arch-32=i686 --with-default-libstdcxx-abi=new --with-gcc-major-version-only --with-multilib-list=m32,m64,mx32 --with-target-system-zlib --with-tune=generic --without-cuda-driver -v
Processor Notes: Scaling Governor: intel_pstate powersave
Security Notes: l1tf: Not affected + meltdown: Mitigation of PTI + spec_store_bypass: Mitigation of SSB disabled via prctl and seccomp + spectre_v1: Mitigation of __user pointer sanitization + spectre_v2: Mitigation of Enhanced IBRS IBPB: conditional RSB filling

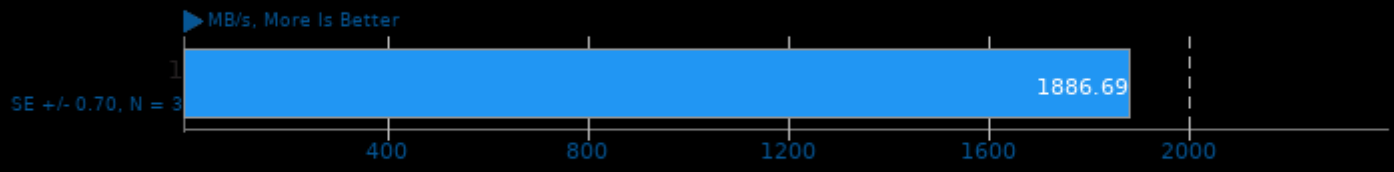
	1
CacheBench - Write Cache (MB/s)	6215
Standard Deviation	0%
CacheBench - Read Cache (MB/s)	1887
Standard Deviation	0.1%
MBW - M.C.F.B.S - 1024 MiB (MiB/s)	4587
Standard Deviation	0.3%
MBW - Memory Copy - 1024 MiB (MiB/s)	5548
Standard Deviation	0.1%
Tinymembench - Standard Memset (MB/s)	7585
Standard Deviation	0%
Tinymembench - Standard Memcpy (MB/s)	5655
Standard Deviation	0%
Stream - Scale (MB/s)	12089
Standard Deviation	1.1%
Stream - Copy (MB/s)	18014
Standard Deviation	1.8%
Stream - Add (MB/s)	12464
Standard Deviation	1.1%
RAMspeed SMP - Floating-Point Add (MB/s)	11688
RAMspeed SMP - Integer Scale (MB/s)	11305
RAMspeed SMP - Integer Copy (MB/s)	11269
RAMspeed SMP - Integer Add (MB/s)	11571

CacheBench
Write Cache



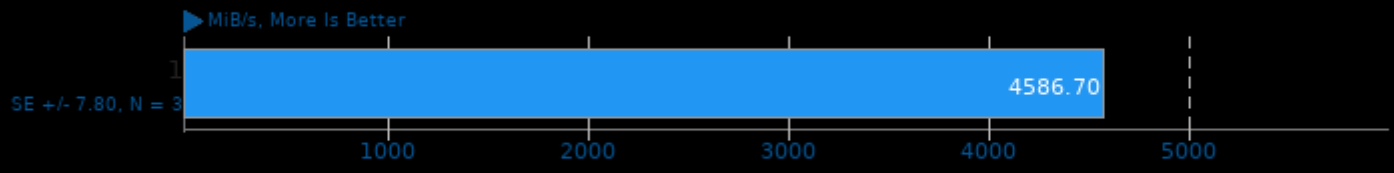
1. (CC) gcc options: -lrt

CacheBench
Read Cache



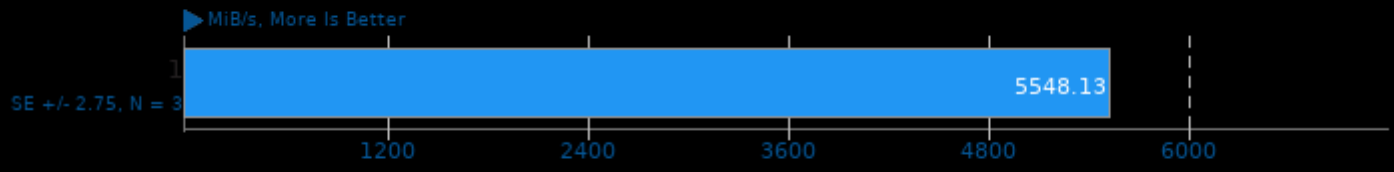
1. (CC) gcc options: -lrt

MBW 2018-09-08
Test: Memory Copy, Fixed Block Size - Array Size: 1024 MiB



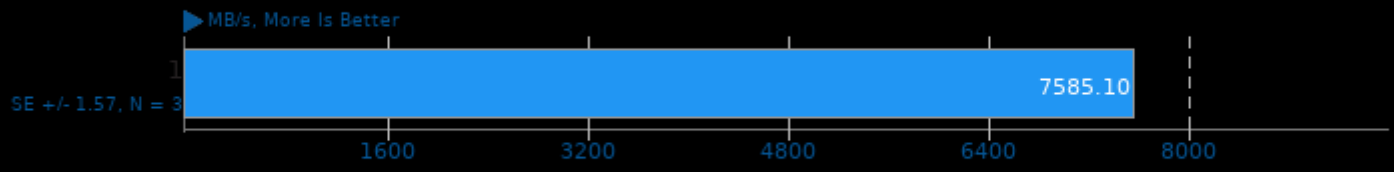
1. (CC) gcc options: -O3 -march=native

MBW 2018-09-08
Test: Memory Copy - Array Size: 1024 MiB



1. (CC) gcc options: -O3 -march=native

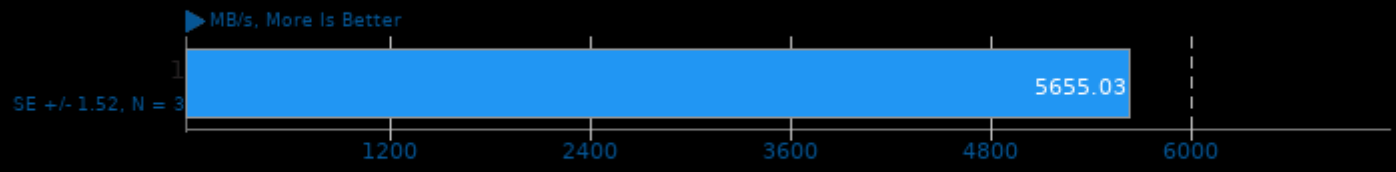
Tinymembench 2018-05-28
Standard Memset



1. (CC) gcc options: -O2 -lm

Tinymembench 2018-05-28

Standard Memcpy



1. (CC) gcc options: -O2 -lm

Stream 2013-01-17

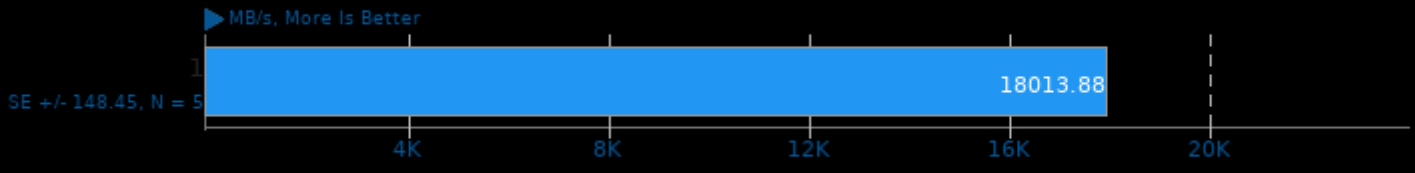
Scale



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

Copy



1. (CC) gcc options: -O3 -march=native -fopenmp

Stream 2013-01-17

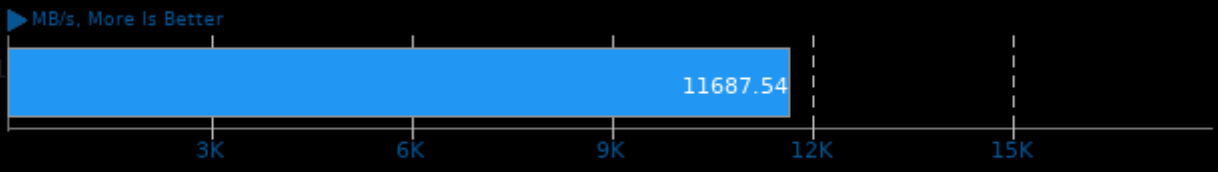
Add



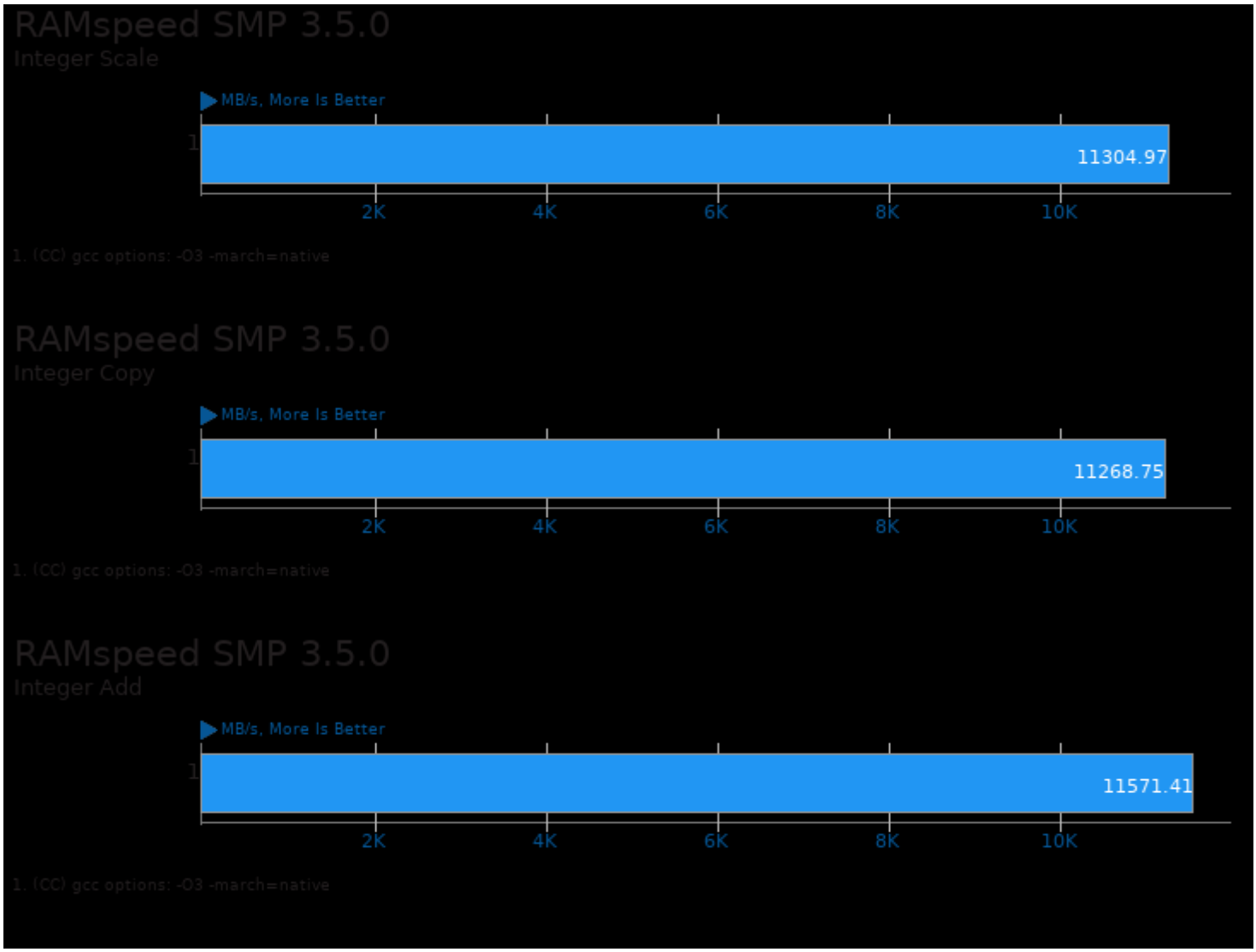
1. (CC) gcc options: -O3 -march=native -fopenmp

RAMspeed SMP 3.5.0

Floating-Point Add



1. (CC) gcc options: -O3 -march=native



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