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cachebench-mlxml1

2 x Intel Xeon E5-2620 v2 testing with a Supermicro X9DRG-HF v0123456789 (3.0a BIOS) and Matrox s MGA G200eW WPCM450 on CentOS 7.3.1611 via the Phoronix Test Suite.

Test Systems:

cachebench

Processor: 2 x Intel Xeon E5-2620 v2 @ 2.60GHz (12 Cores / 24 Threads), Motherboard: Supermicro X9DRG-HF v0123456789 (3.0a BIOS), Chipset: Intel Xeon E7 v2/Xeon, Memory: 32768MB, Disk: 120GB INTEL SSDSC2BB12, Graphics: Matrox s MGA G200eW WPCM450, Network: Intel I350 Gigabit Connection + Mellanox MT27500 Family

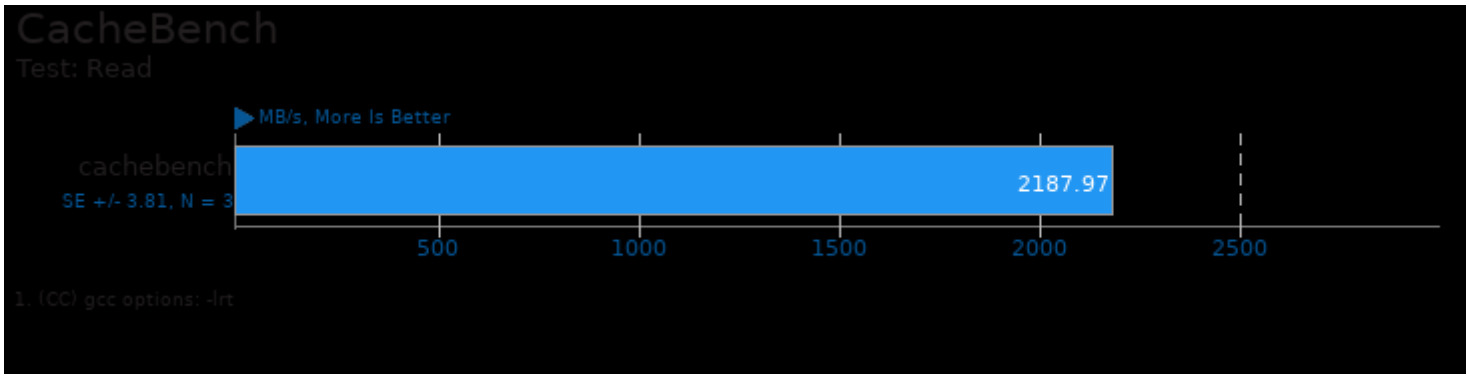
OS: CentOS 7.3.1611, Kernel: 3.10.0-514.6.1.el7.x86_64 (x86_64), Compiler: GCC 4.8.5 20150623, File-System: nfs, Screen Resolution: 1024x768

Processor Notes: Scaling Governor: intel_pstate powersave

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	cachebench
CacheBench - Read (MB/s)	2188
Standard Deviation	0.3%

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This file was automatically generated via the Phoronix Test Suite benchmarking software on Thursday, 28 March 2024 20:17.